

# Self-Assembly of Decidable Sets

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Iowa State University, USA

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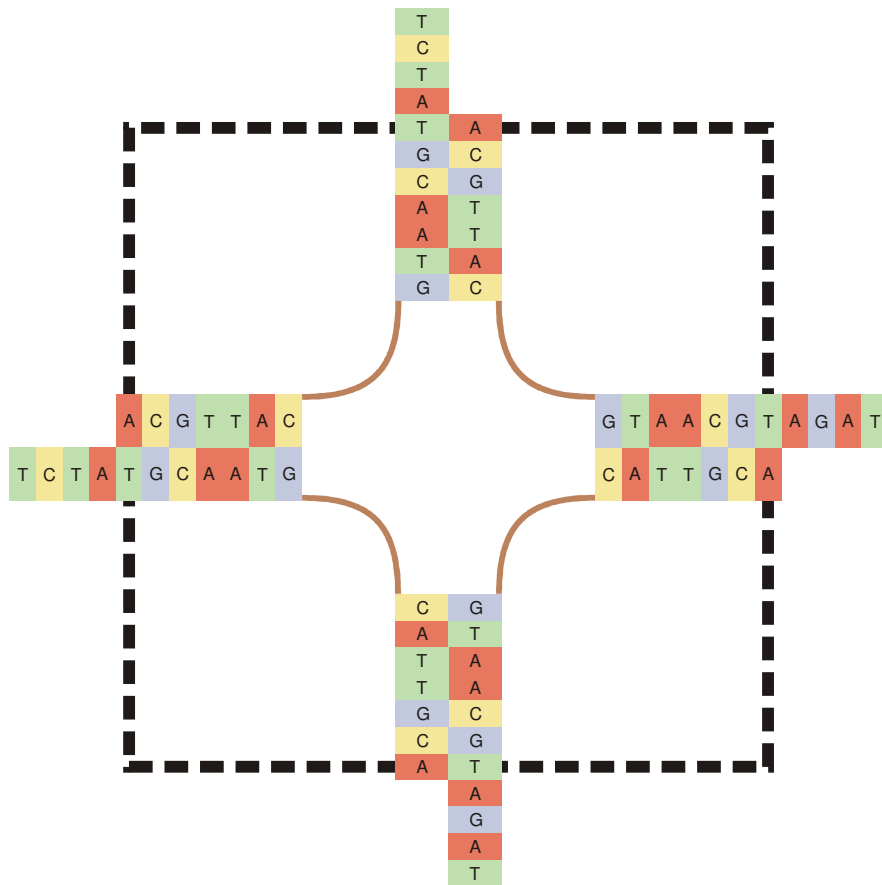
# Outline of Presentation

- Overview of Tile Assembly Model
- The wedge construction
- A new characterization of decidable languages
- Analysis of space requirements

# DNA Tile Self-Assembly

## Seeman, starting in 1980s

DNA tile, oversimplified:

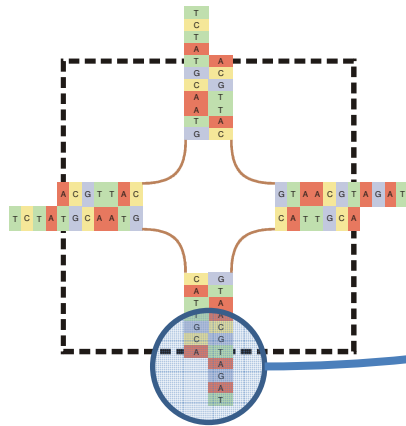


Four single DNA strands bound by Watson-Crick pairing (A-T, C-G).

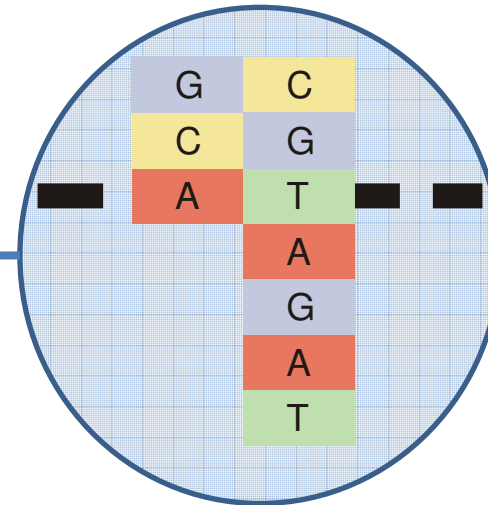
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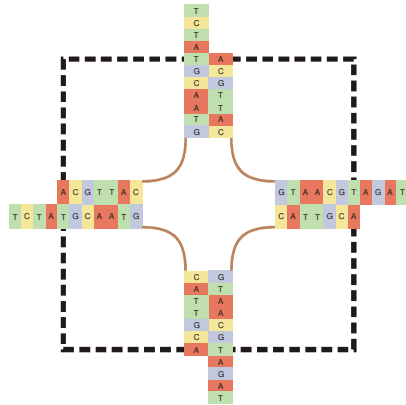


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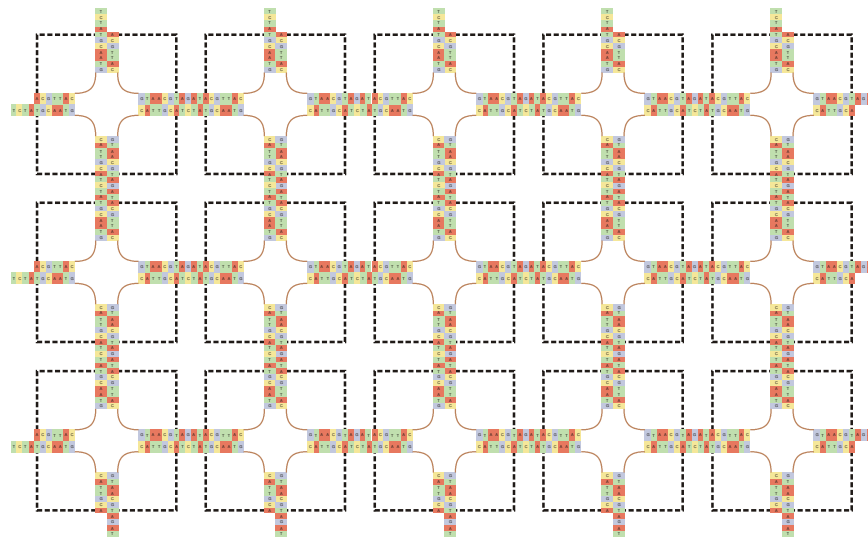
DNA tile, oversimplified:



Four single DNA strands bound by Watson-Crick pairing (A-T, C-G).

Choice of sticky ends allows one to *program* the pattern of the array.

“Sticky ends” bind with their Watson-Crick complements, so that a regular array self-assembles.



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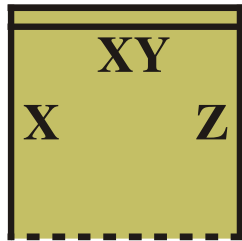
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..... Strength 0

————— Strength 1

==== Strength 2

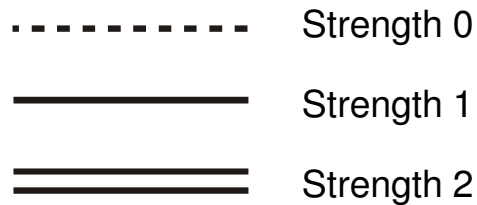
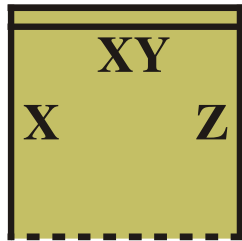


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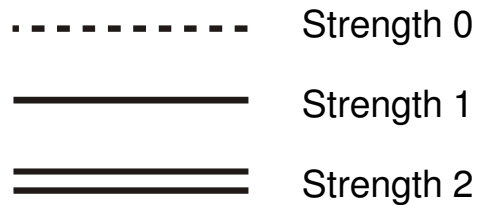
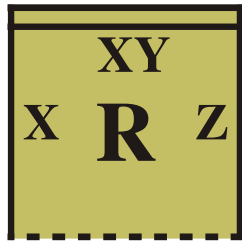
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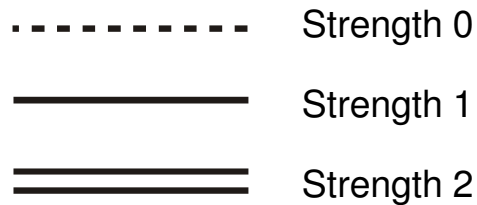
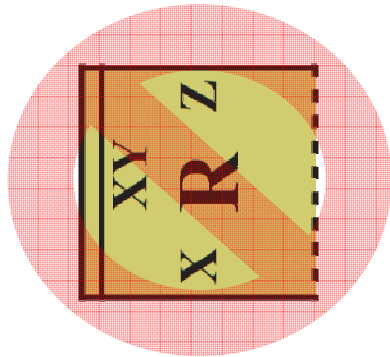
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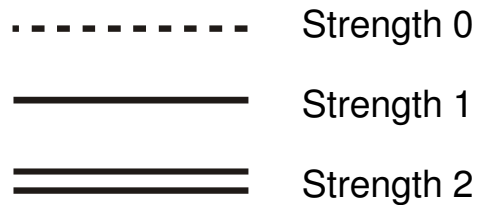
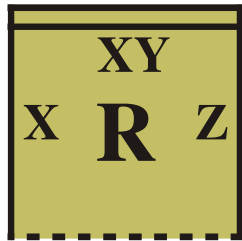
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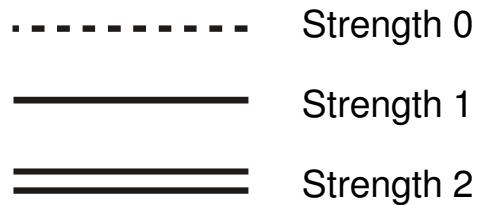
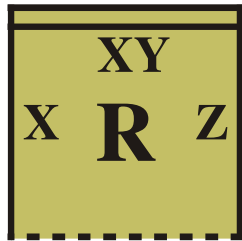
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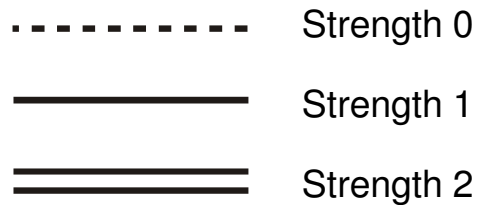
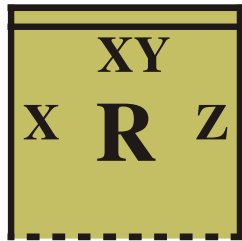
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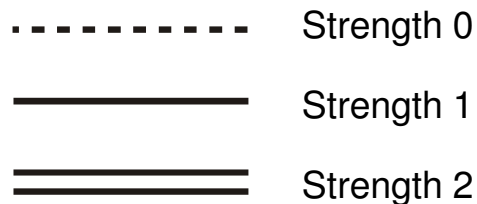
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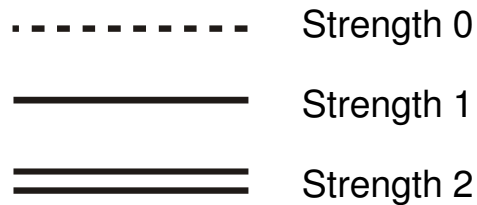
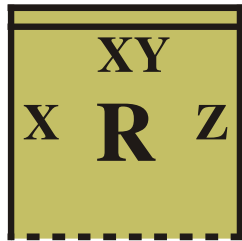
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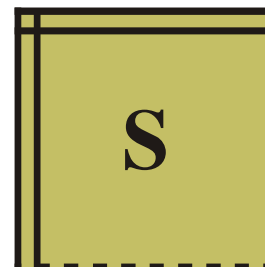
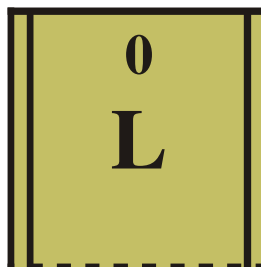
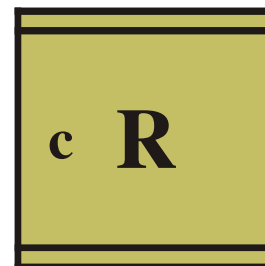
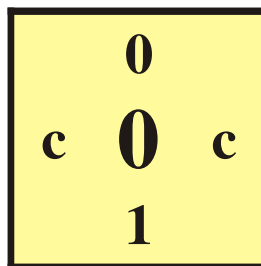
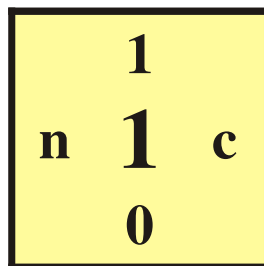
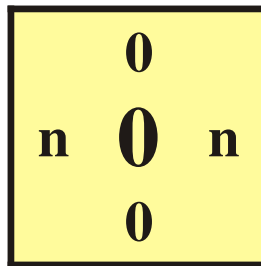
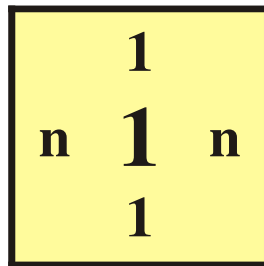
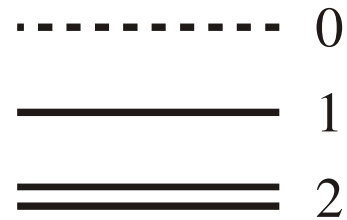
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**NEXT:** An example...



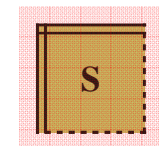
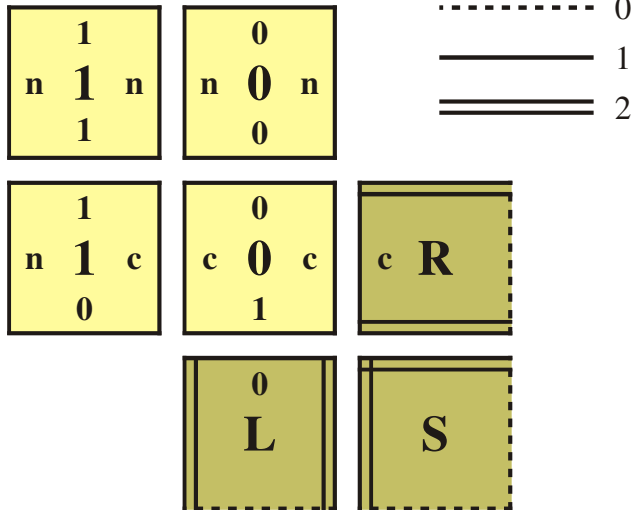
# Tile Assembly Example

Edge binding strengths:



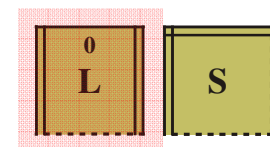
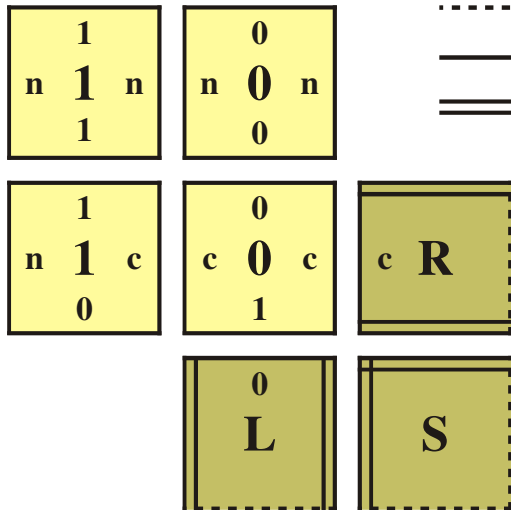
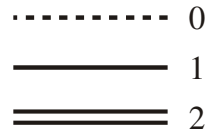
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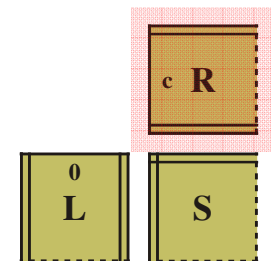
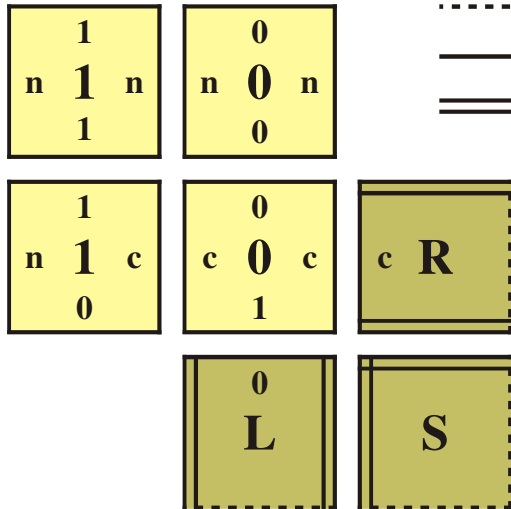
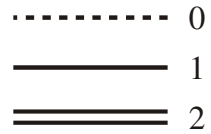
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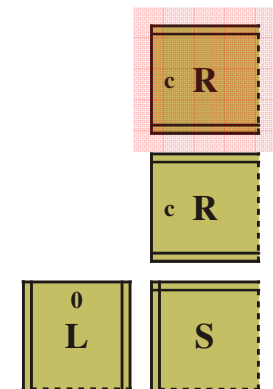
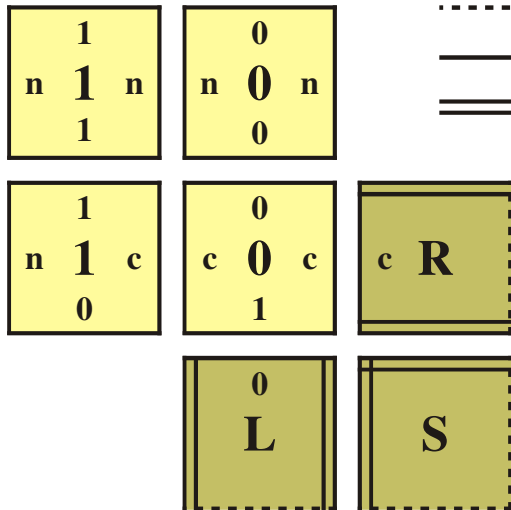
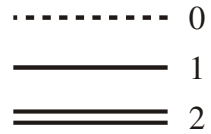
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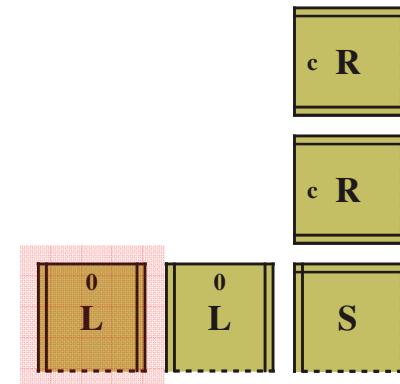
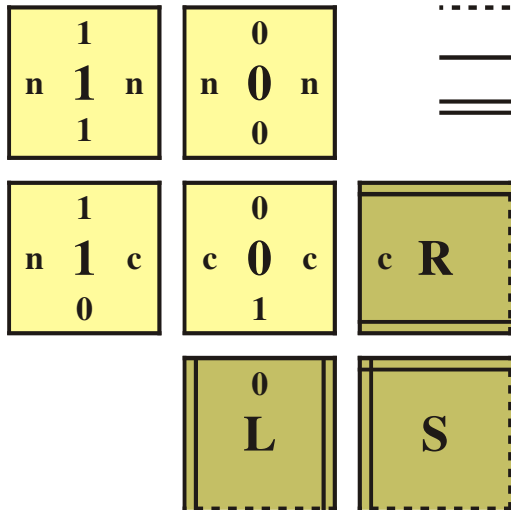
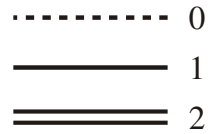
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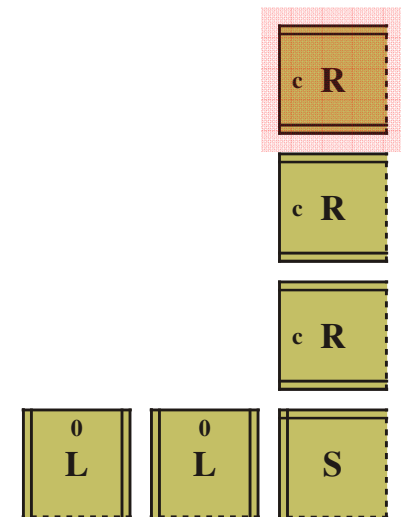
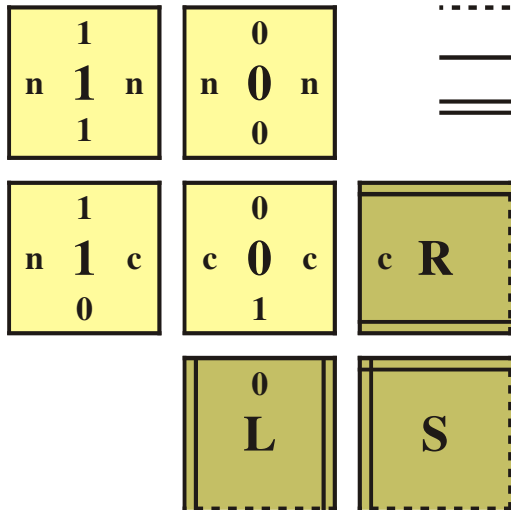
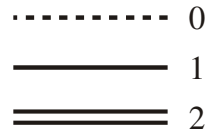
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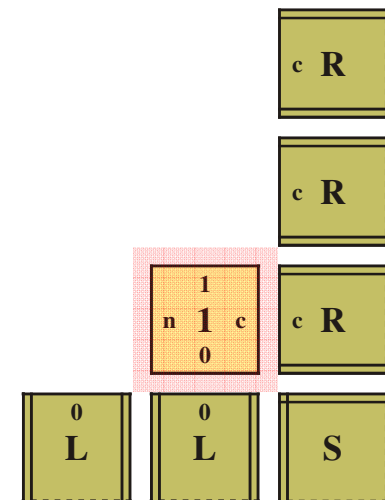
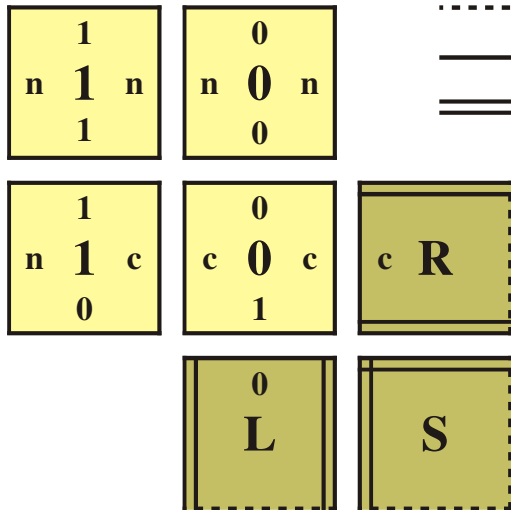
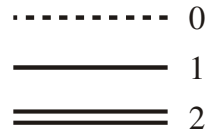
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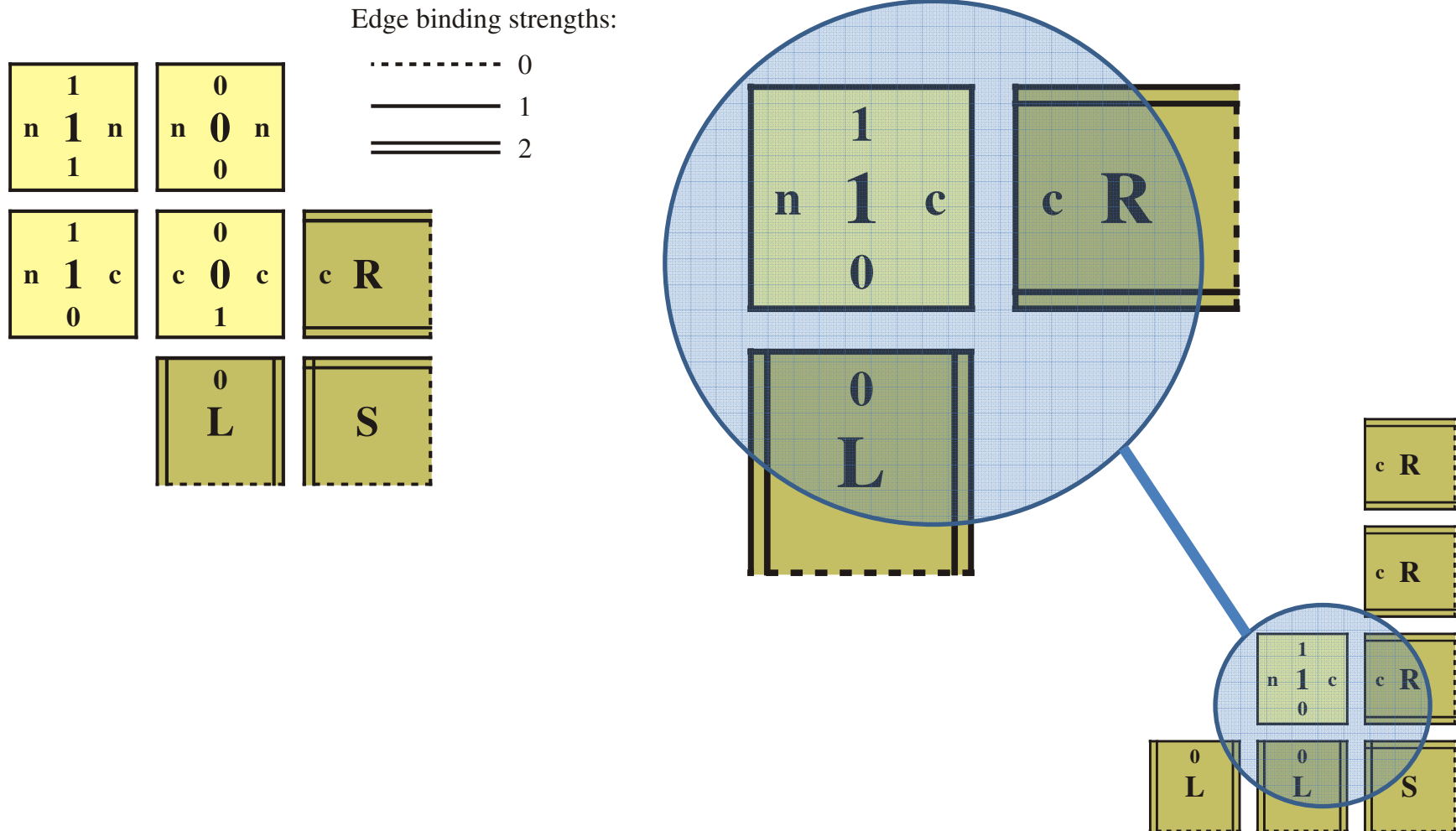
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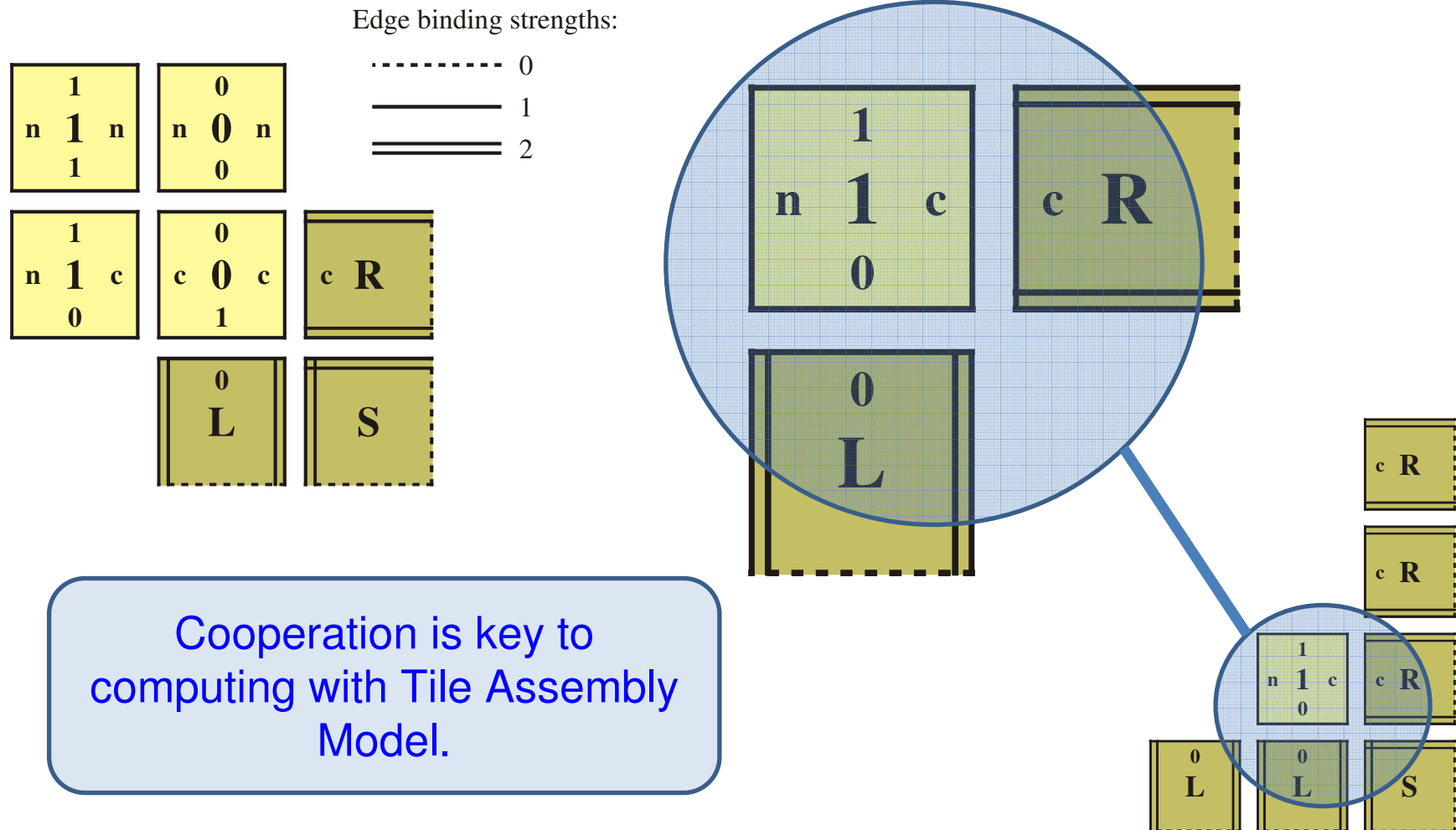




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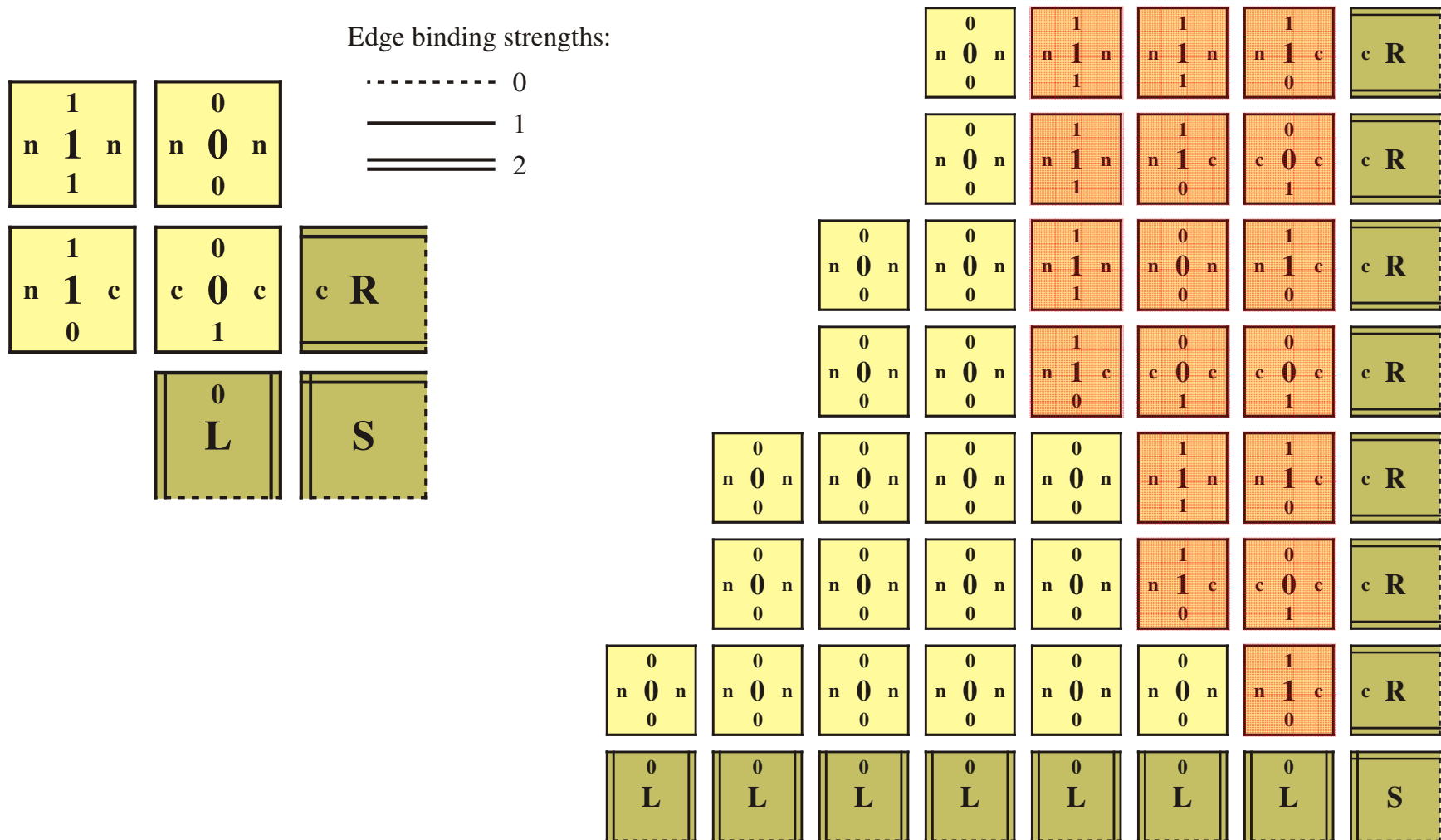


# Tile Assembly Example



Cooperation is key to computing with Tile Assembly Model.

# Tile Assembly Example



# Our Results

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First we'll define two supplementary constructions...

# The Wedge Construction

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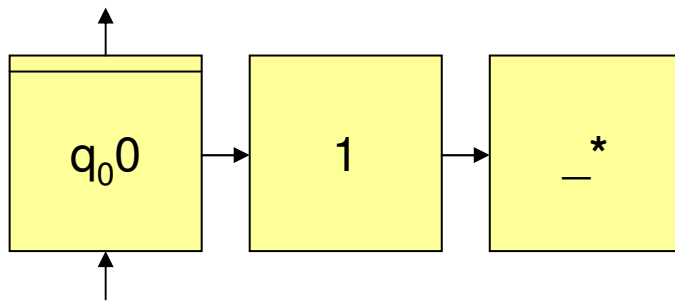
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# The Wedge Construction

- Named for the shape of the assembly
- Utilizes well known technique of simulating a Turing machine with a tile assembly
  - Every row of the assembly encodes the entire configuration of the Turing machine (tape and state) at a particular step in the computation
  - The assembly simulates a 'one-way infinite to the right' tape by adding a tile on the right side of the row for each subsequent computation step

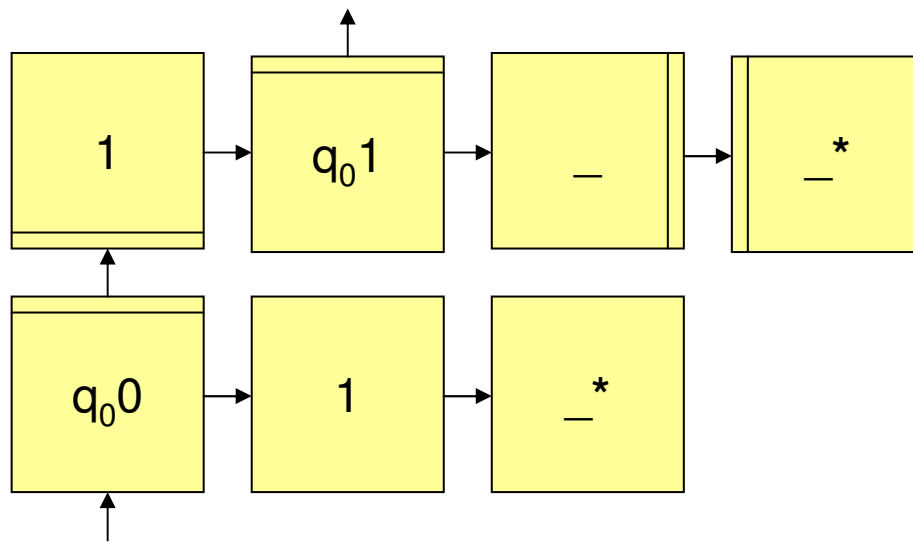
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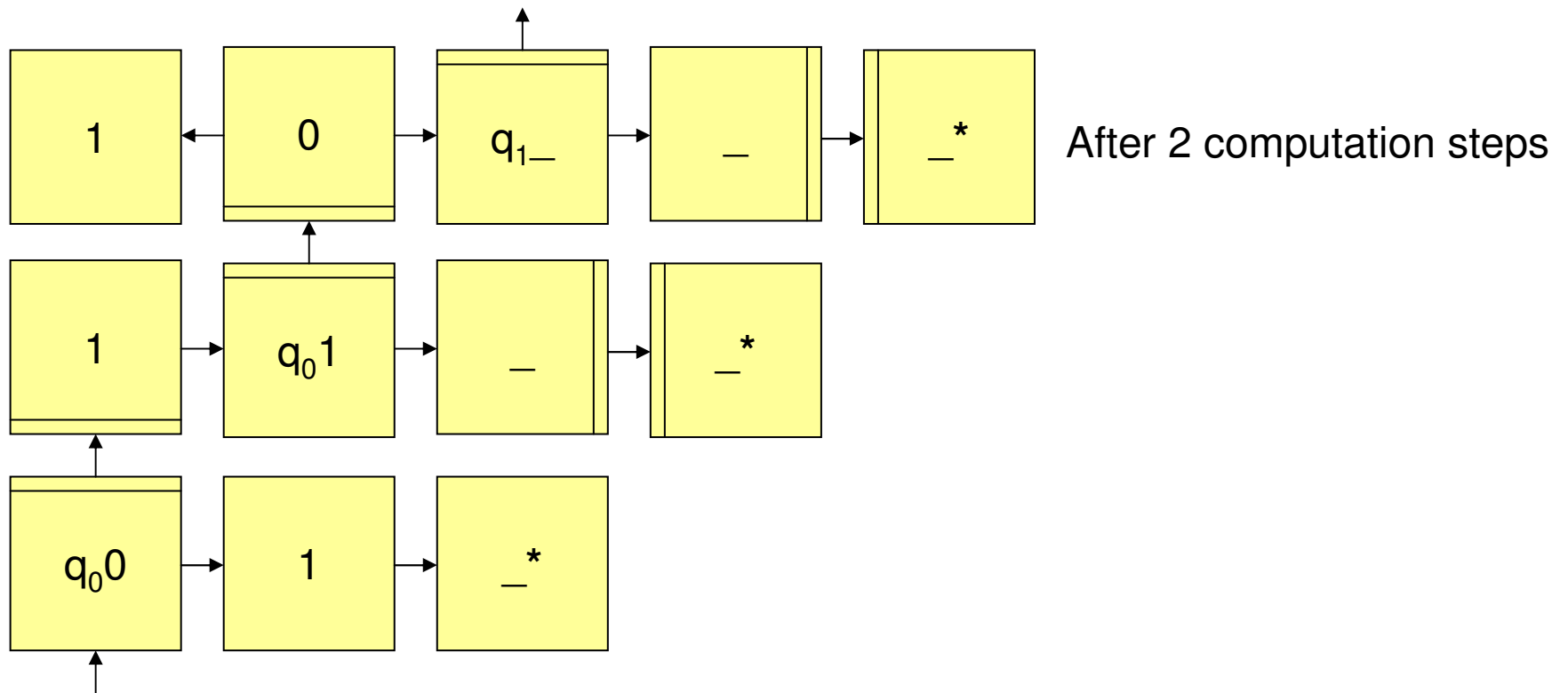
Initial configuration: TM on input '01'

# Wedge Construction Example

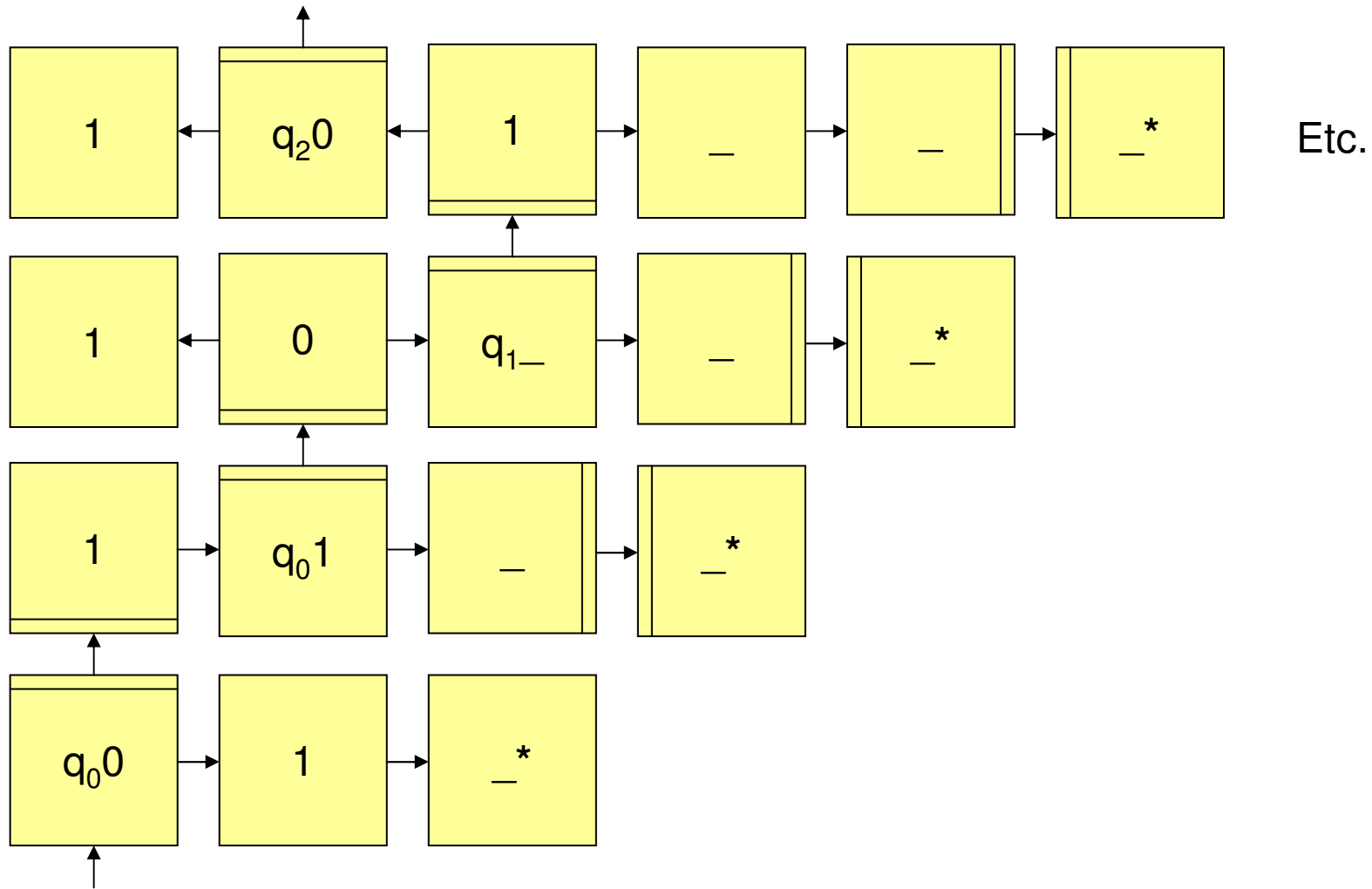


After 1 computation step

# Wedge Construction Example



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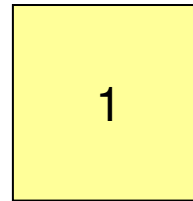




# Log-Width Binary Counter

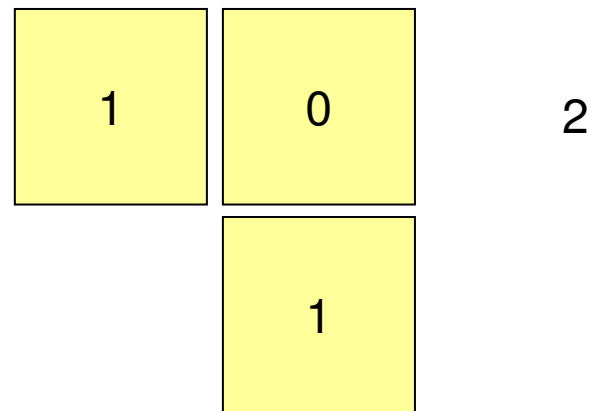
- An assembly which simulates a binary counter from 0 to infinity as it grows upward
- Each row represents a single value, which is one greater than the value of the row beneath it
- The width of each row is equal to the (floor of) the  $\log_2$  of the counter's value in that row

# Log-Width Binary Counter Example

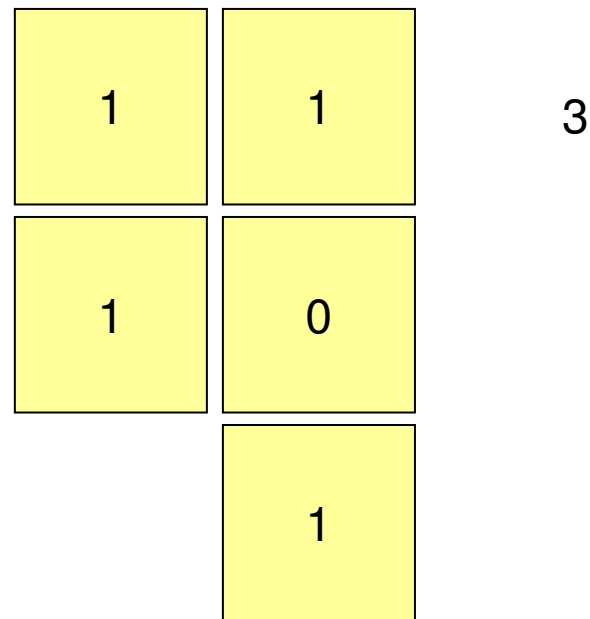


1

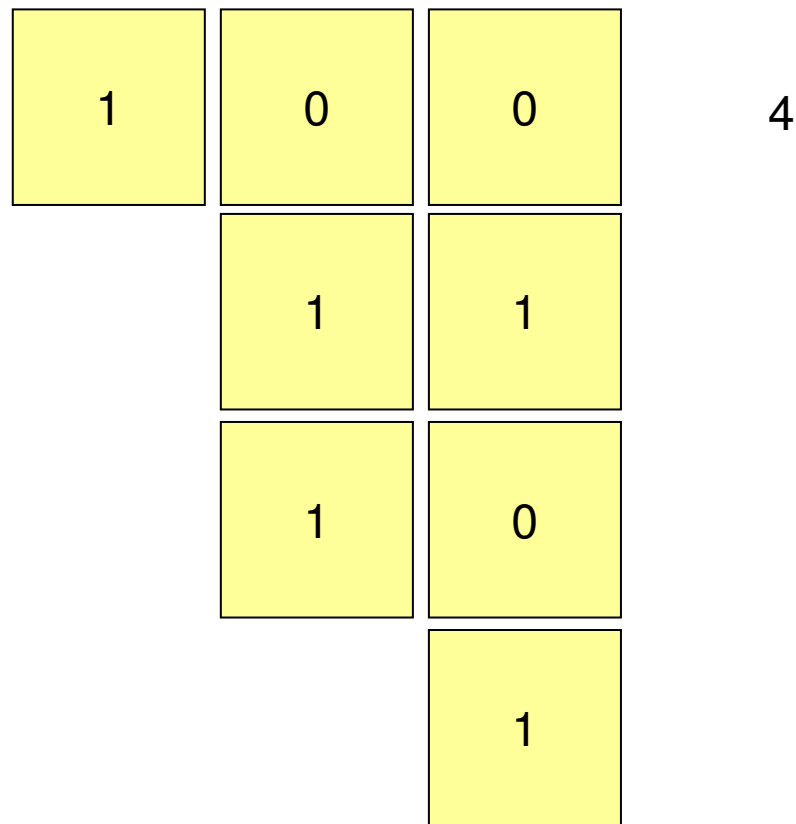
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1	0	0	0	0
	1	1	1	1
	1	1	1	0
	1	1	0	1
	1	1	0	0
	1	0	1	1
	1	0	1	0
	1	0	0	1
	1	0	0	0
		1	1	1
		1	1	0
		1	0	1
		1	0	0
			1	1
			1	0
				1

# A New Characterization of Decidable Languages

Theorem: Let  $A \subseteq \mathbf{N}$ . The set  $A$  is decidable if and only if  $A \times \{0\}$  and  $A^c \times \{0\}$  weakly self-assemble.

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Theorem: Let  $A \subseteq \mathbf{N}$ . The set  $A$  is decidable if and only if  $A \times \{0\}$  and  $A^c \times \{0\}$  weakly self-assemble.

Proof: ( $\rightarrow$ ) This direction of our proof consists of a construction that demonstrates the claim.



# Construction Overview

Fact: If  $A$  is decidable, then there exists a TM  $M$  which halts on every input and accepts exactly those that are in  $A$

# Construction Overview

```
while  $0 \leq n < \infty$  do
  simulate  $M$  on the binary representation of  $n$ 
  if  $M$  accepts, then
    output 1
  else
    output 0
  end if
   $n := n + 1$ 
end while
```

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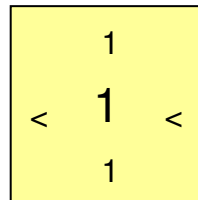
Our construction implements that algorithm by stacking wedge constructions on top of each other

Each wedge construction simulates  $M$  on an input value one greater than the wedge construction below it

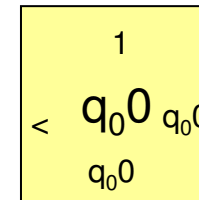
Incrementing the inputs is done by embedding a log-width binary counter within the wedge constructions

# Construction Technique: Embedding Functionality

Log-width binary  
counter tile

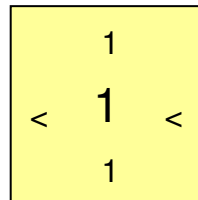


Turing machine  
tile

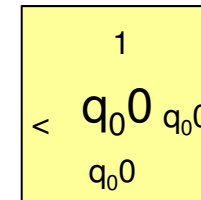


# Construction Technique: Embedding Functionality

Log-width binary  
counter tile



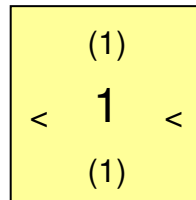
Turing machine  
tile



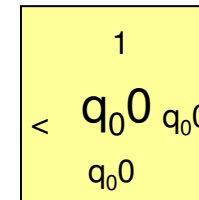
Turing machine tile passing  
binary counter value upward

# Construction Technique: Embedding Functionality

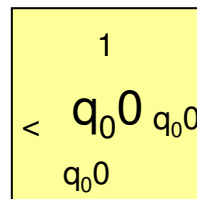
Log-width binary  
counter tile



Turing machine  
tile

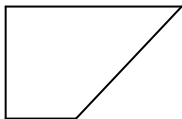


Turing machine tile passing  
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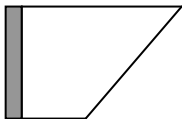


# Construction Diagram



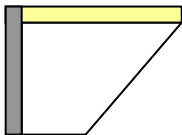
Start with a wedge construction  
which simulates TM  $M$  on input 0

# Construction Diagram



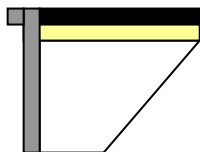
Embed a log-width binary counter along the left side

# Construction Diagram



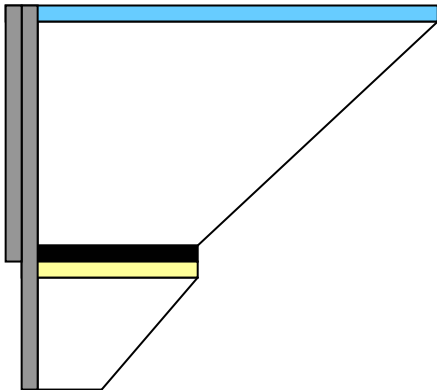
Once  $M(0)$  halts and accepts or rejects,  
make a row specifying the result

# Construction Diagram



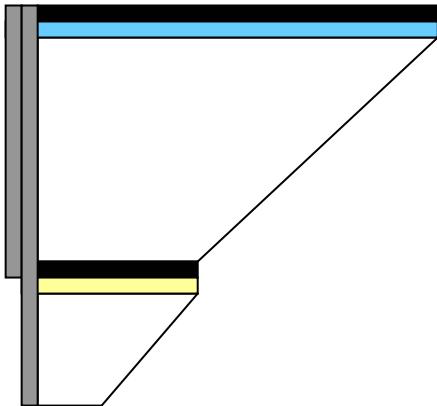
Next, add a row which increments the counter

# Construction Diagram



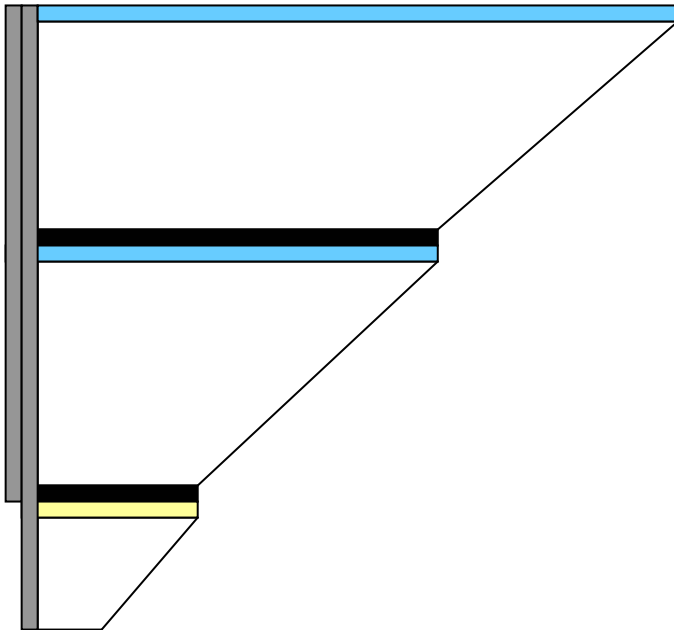
Use the new counter value  
to begin the simulation  
of  $M(1)$

# Construction Diagram



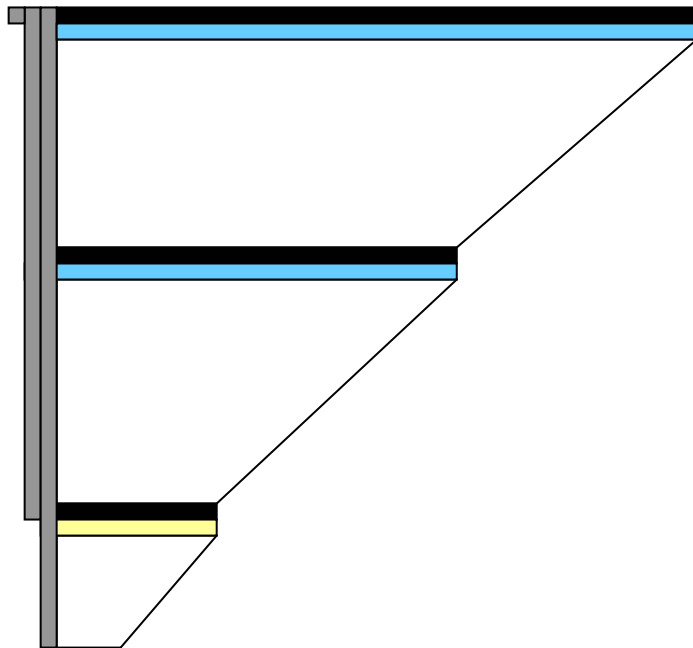
Increment the counter

# Construction Diagram



Simulate  $M(2)$

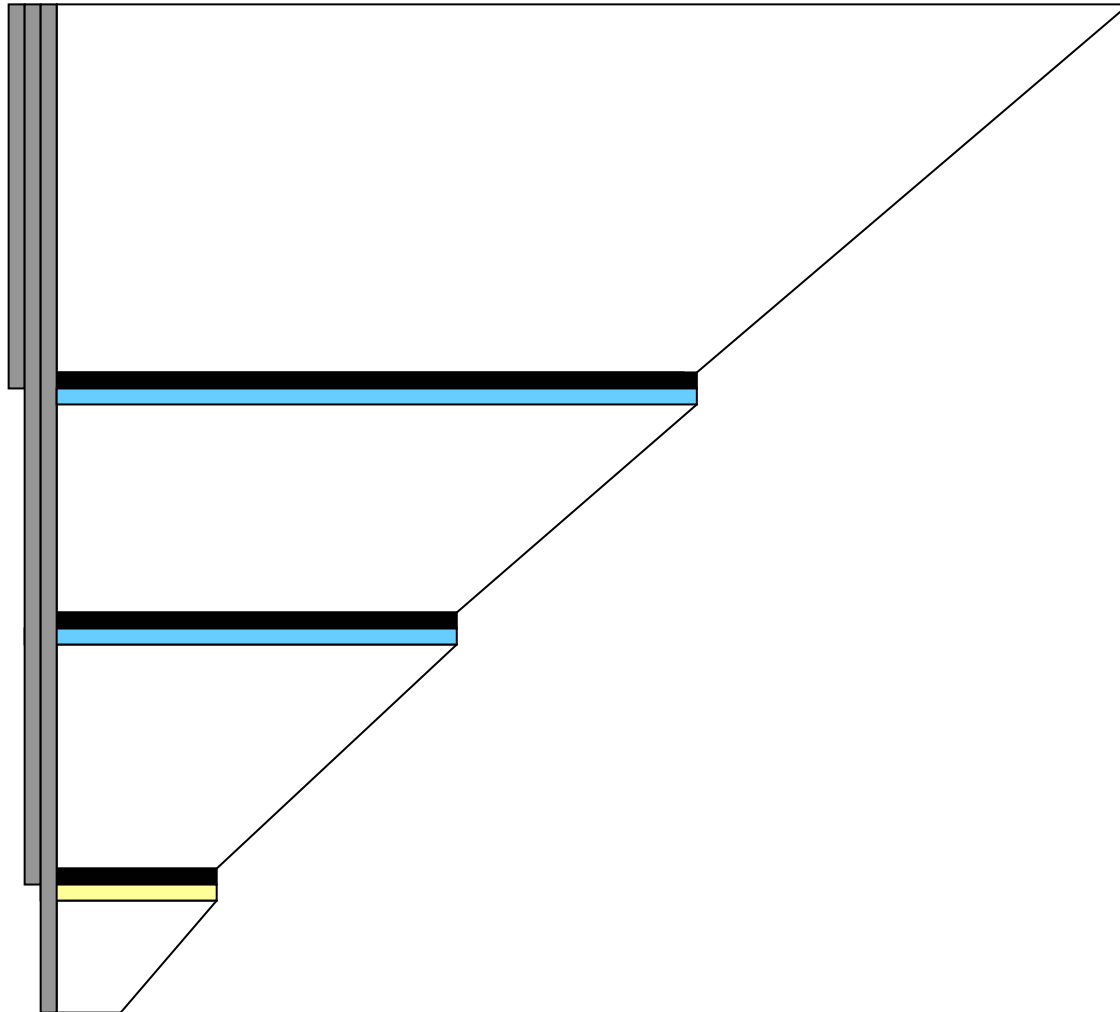
# Construction Diagram



Increment the counter

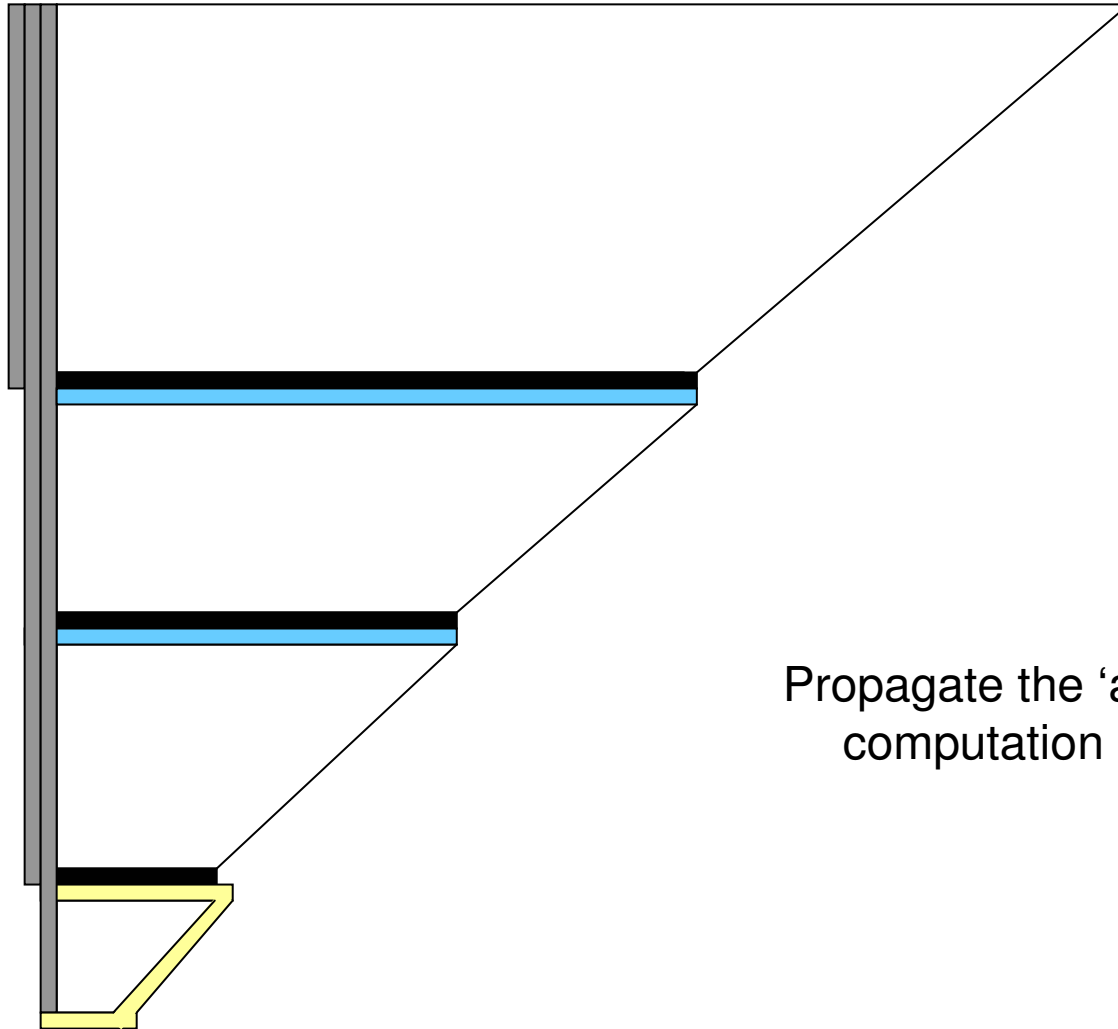


# Construction Diagram



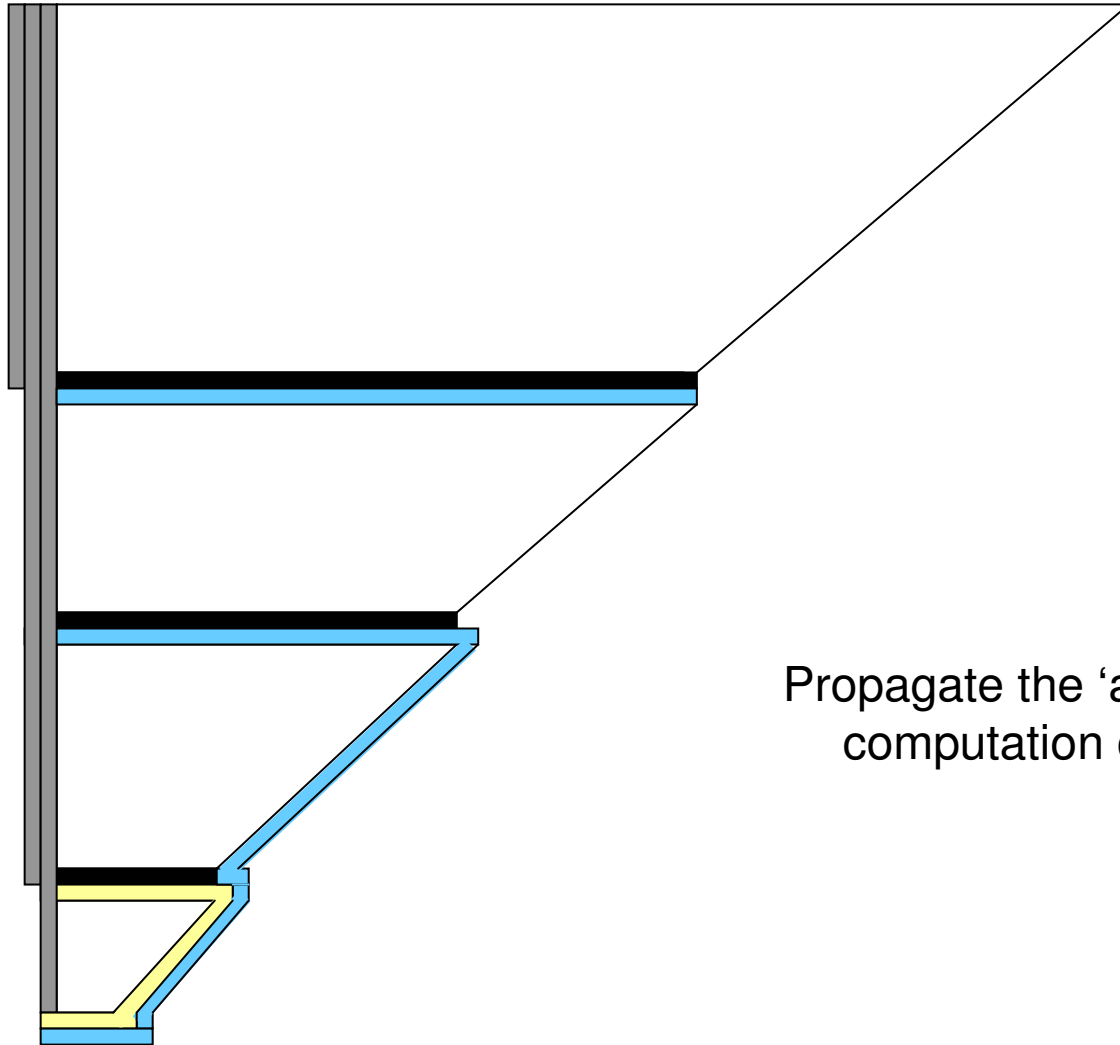
Etc.

# Construction Diagram



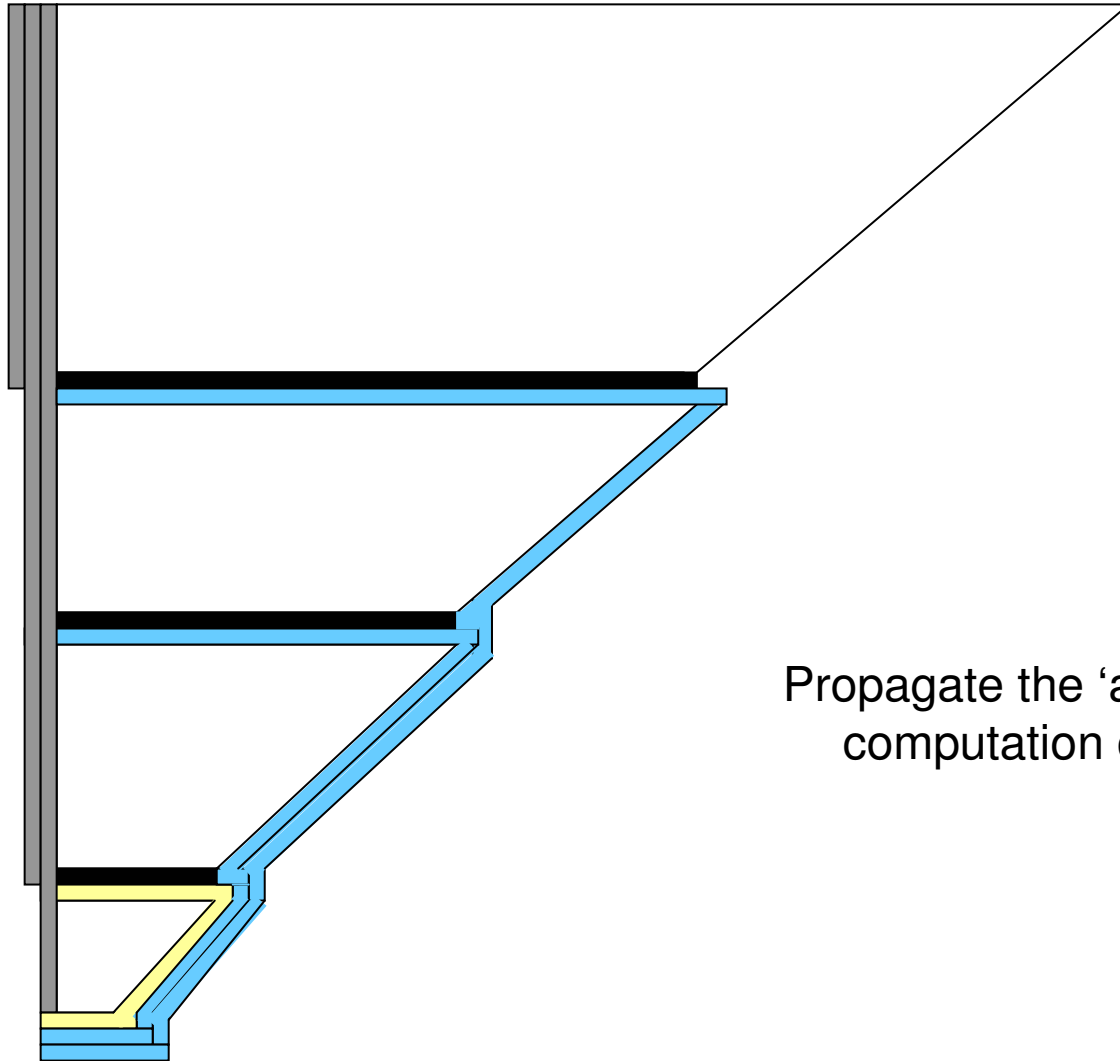
Propagate the 'answers' from each computation down to the axis

# Construction Diagram



Propagate the 'answers' from each computation down to the axis

# Construction Diagram



Propagate the 'answers' from each computation down to the axis

# Construction Example

ISU TAS Simulation Window: simpleTM.tdp (C:\src\patitz\1\DeciderTiler\test\simpleTM.tdp)

File Appearance Option Control About

10%

Tile Type X Simulation space

~(M0\*L)  
SEED  
SEED  
SOLN

(X, Y, Z)  
0 1 0

SEED

Label: SEED  
North: [2] ~(M0\*L)  
East: [2] SEEDE  
South: [1] SOLN  
West: [0] -99

Seed tile  
Apply Center

Overview X

Messages

Output Debug

806 tiletypes being loaded into simulator...  
This tileset is very large, so some seed assembly editing enhancements are disabled.  
Tile set 'simpleTM.tds' successfully loaded into simulator with 806 tile type definitions.  
Resetting tile assembly space.  
Tile space 'simpleTM.tdp' loaded into simulator.

Simulation steps: 9172 | Tiles in assembly: 9173 | X:0 Y:1

# A New Characterization of Decidable Languages

This construction proves that if a set is decidable, then  $A \times \{0\}$  and  $A^c \times \{0\}$  weakly self-assemble.

# A New Characterization of Decidable Languages

Proof: ( $\leftarrow$ ) This direction of the proof uses the existence of self-assembly simulators to prove that if  $A \times \{0\}$  and  $A^c \times \{0\}$  weakly self-assemble, then the set  $A$  is decidable.

# Proof Sketch ( $\leftarrow$ )

- Assume  $A$  and  $A^c$  both weakly self-assemble



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- For an input  $n$ , simulate both tile assembly systems in parallel
  - Accept if  $\mathcal{T}_{A \times \{0\}}$  puts a black tile at  $(n,0)$
  - Reject if  $\mathcal{T}_{A^c \times \{0\}}$  puts a black tile at  $(n,0)$

# A New Characterization of Decidable Languages

This completes the proof that a set  $A$  is decidable if and only if  $A \times \{0\}$  and  $A^c \times \{0\}$  weakly self-assemble.

# Second Main Result

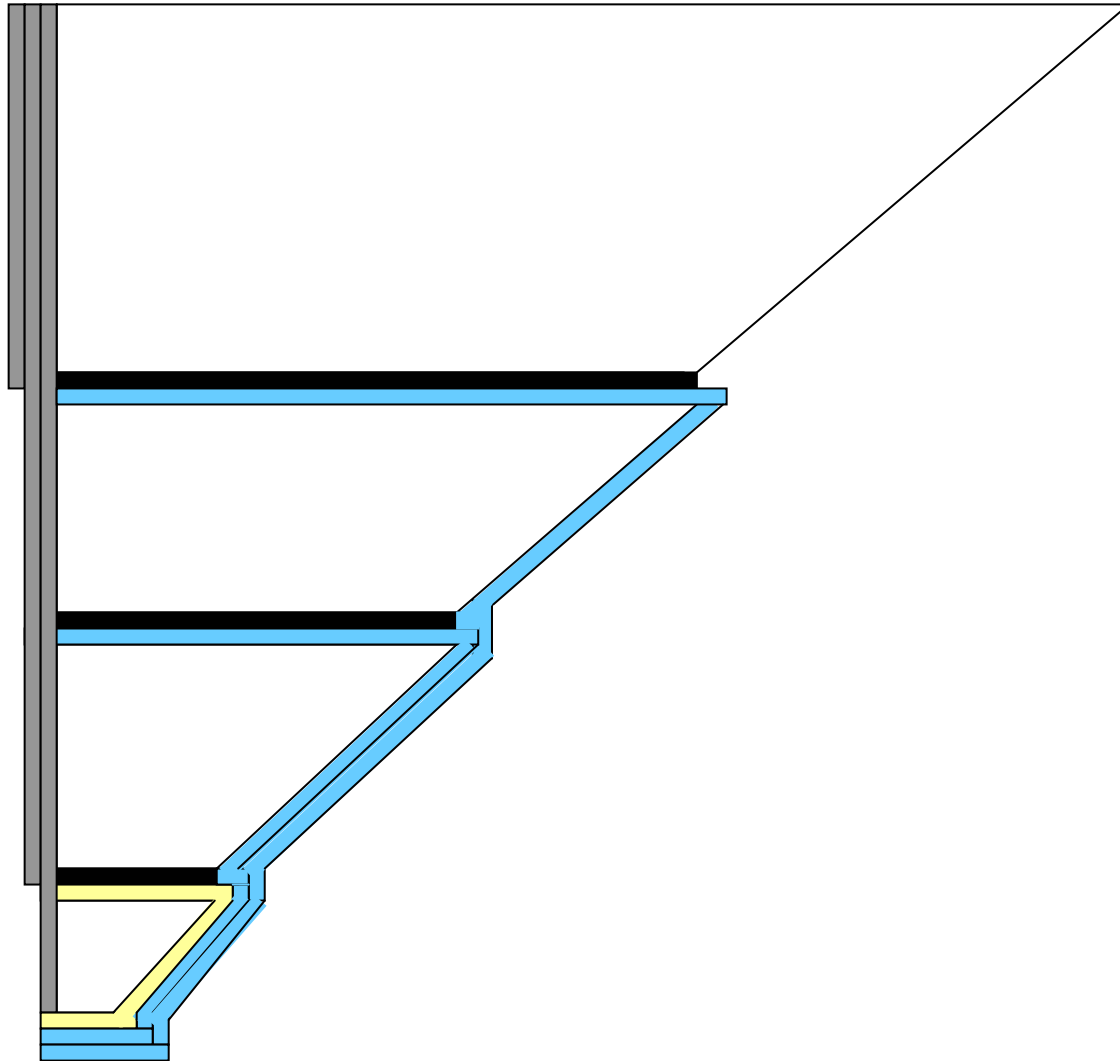
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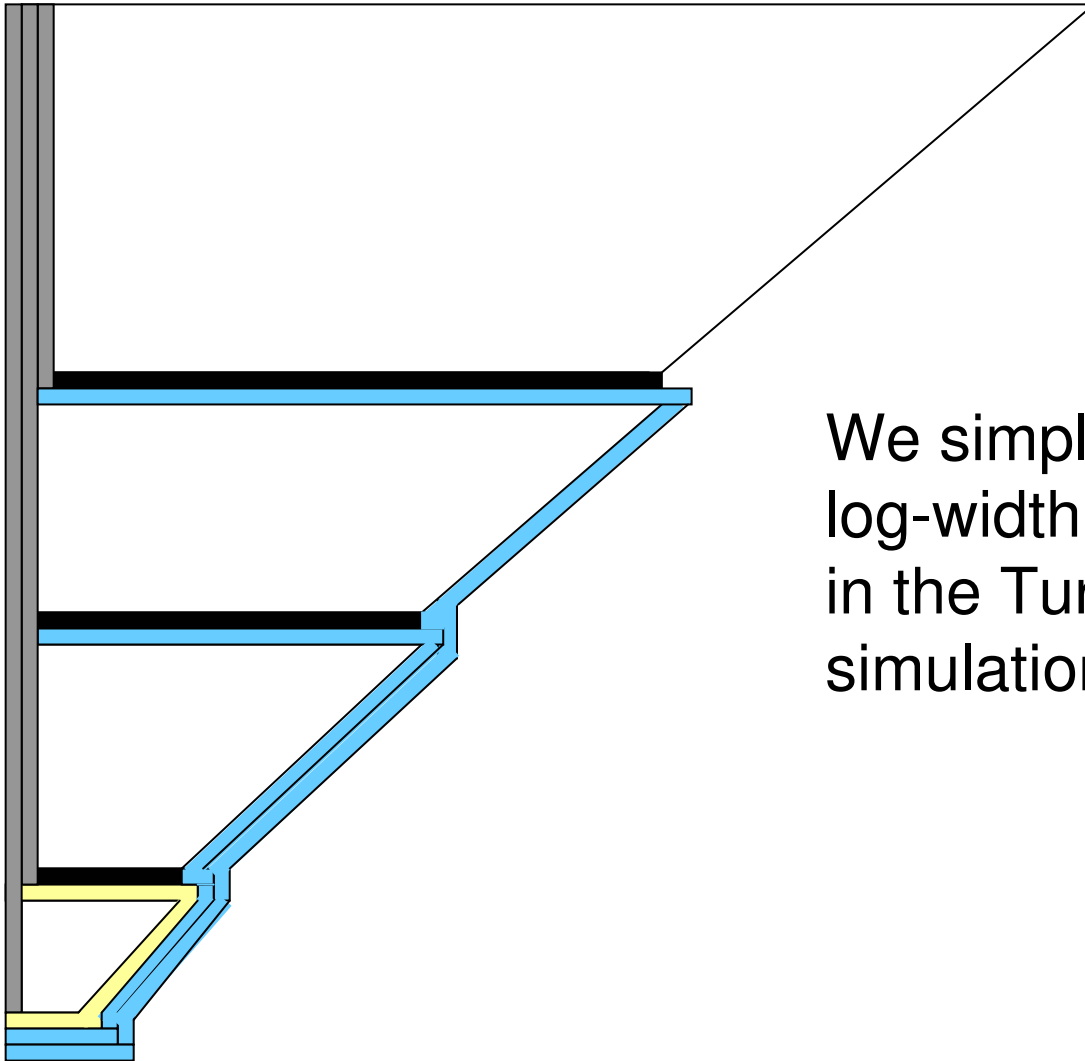
Note that it *is* possible to prove our first main result while placing tiles in only **two quadrants**.

# Two quadrants





# Two quadrants



We simply embed the log-width binary counter in the Turing machine simulation.

# Second Main Result

However, if the language  $A$  has sufficient space complexity, **AND** your tile assembly system *resembles* our construction in the sense that the TM is simulated “row by row,” then two quadrants of space are **necessary**.

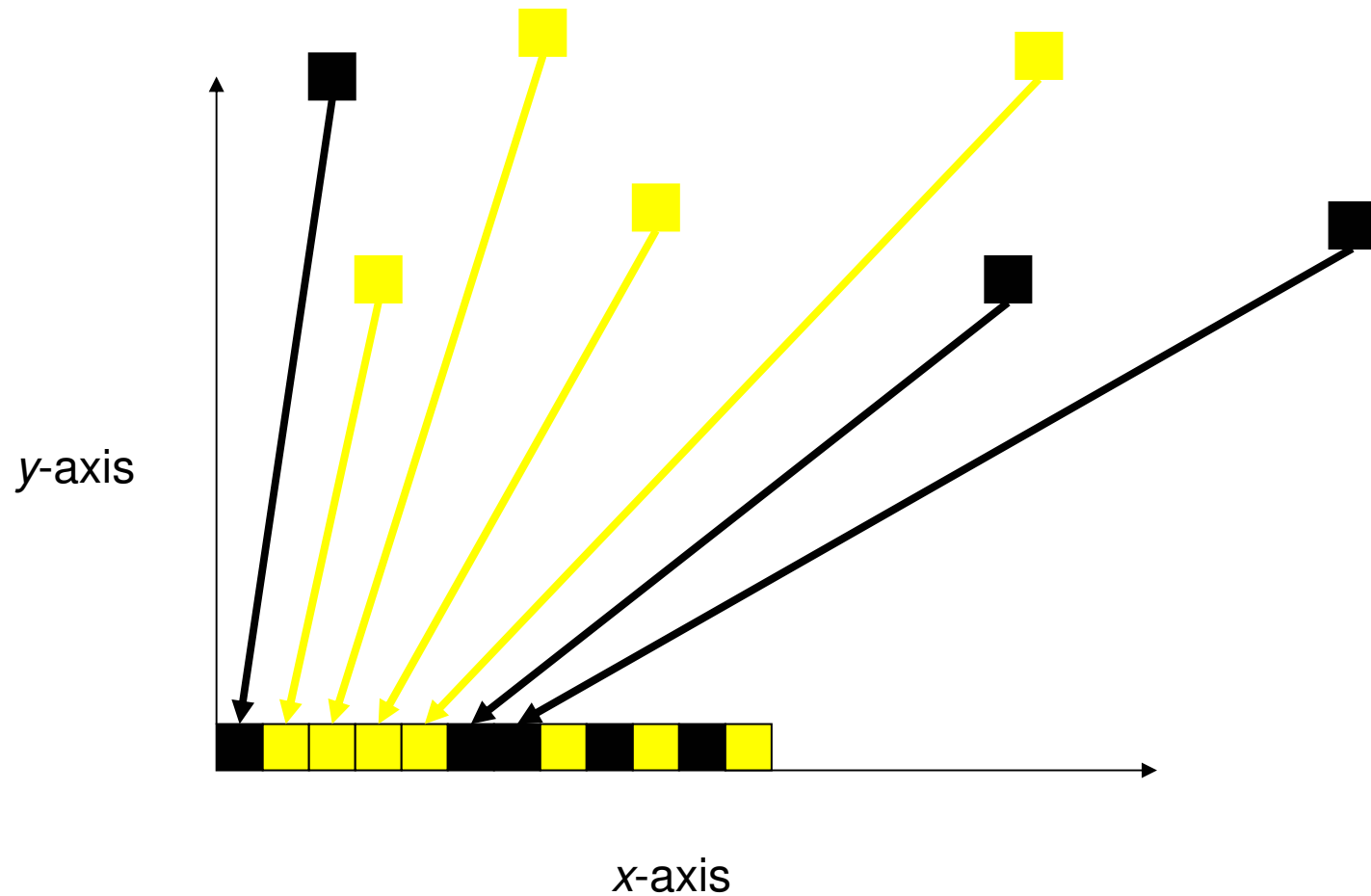
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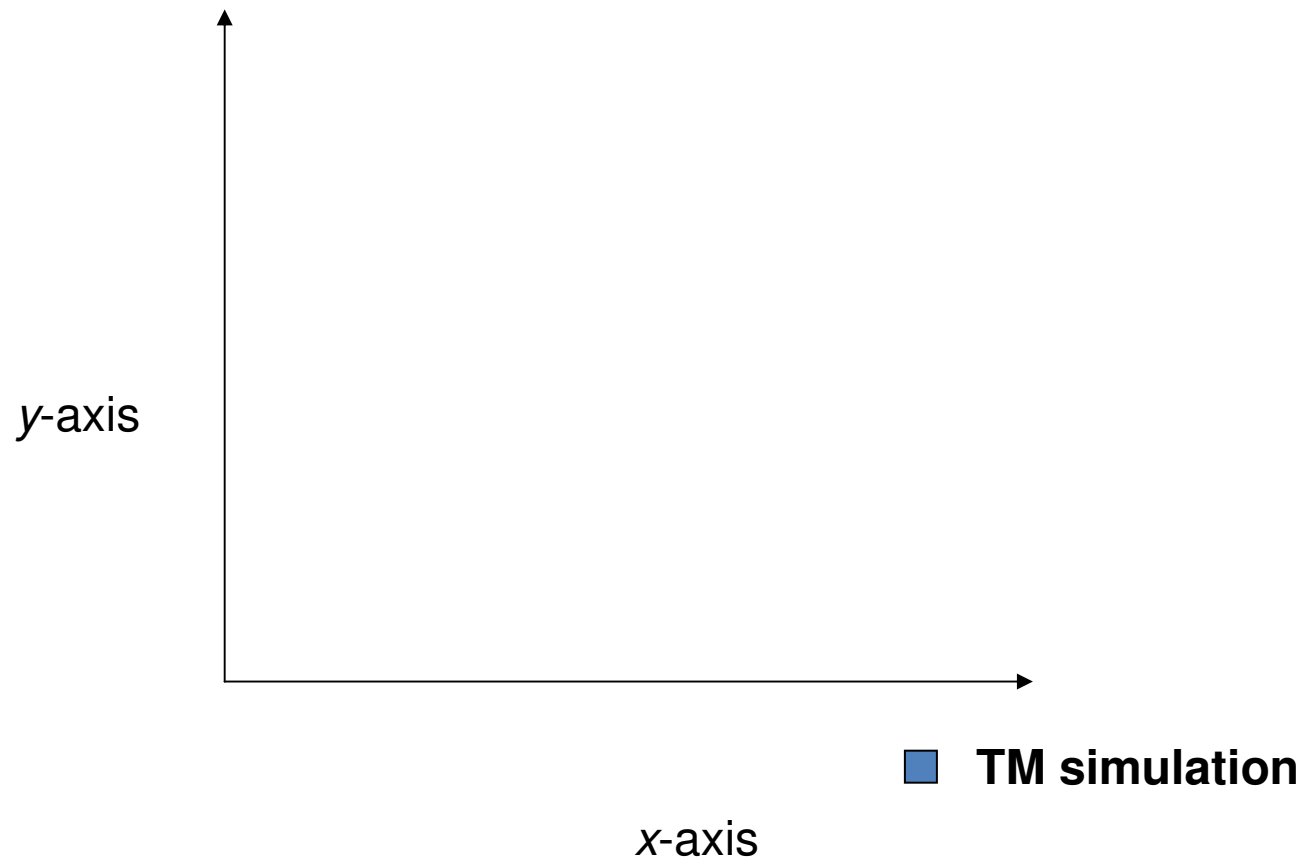
# Second Main Result (proof idea)



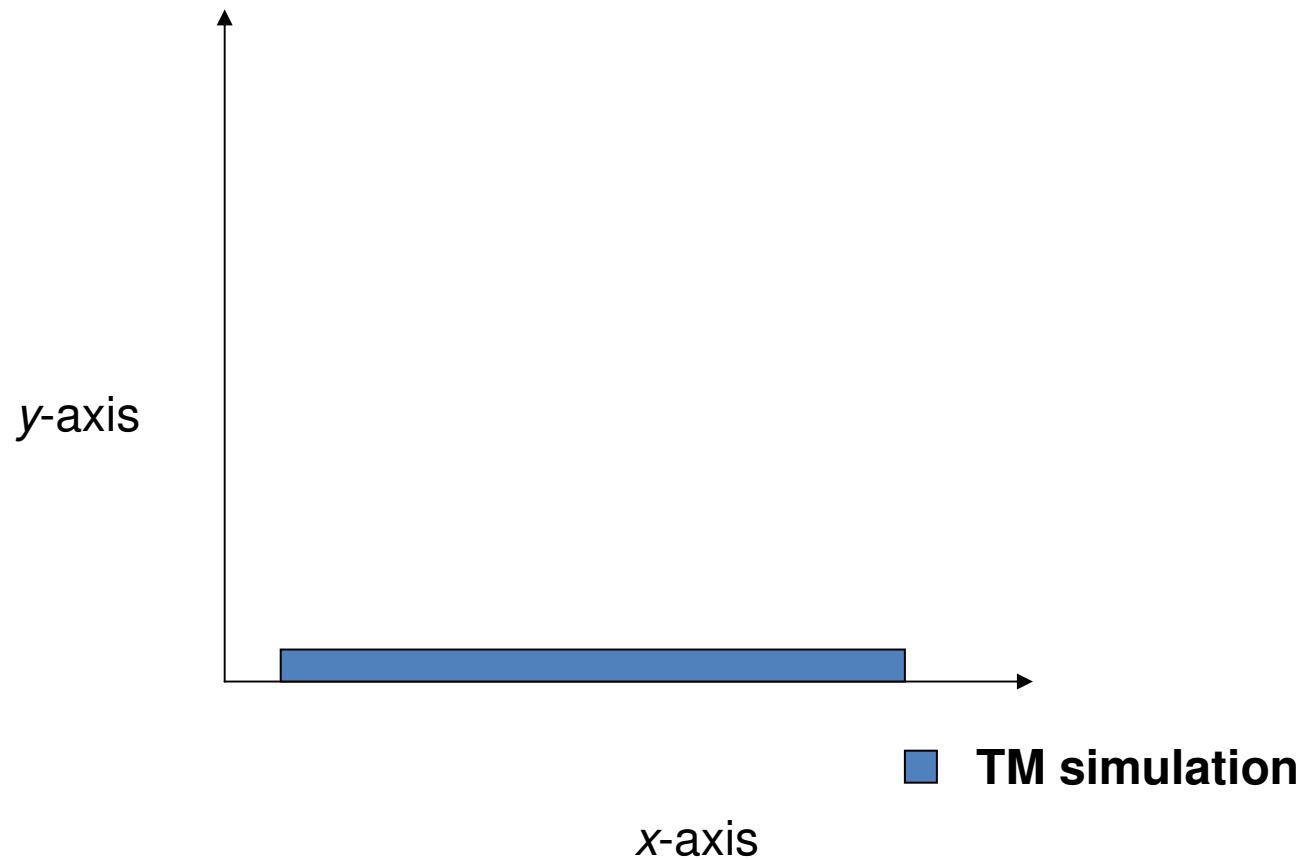
# Second Main Result (proof idea)

Assumption #2: aside from all of the paths mentioned on the previous slide, the rest of the assembly can be self-assembled (entirely) one row at a time

# Second Main Result (proof idea)

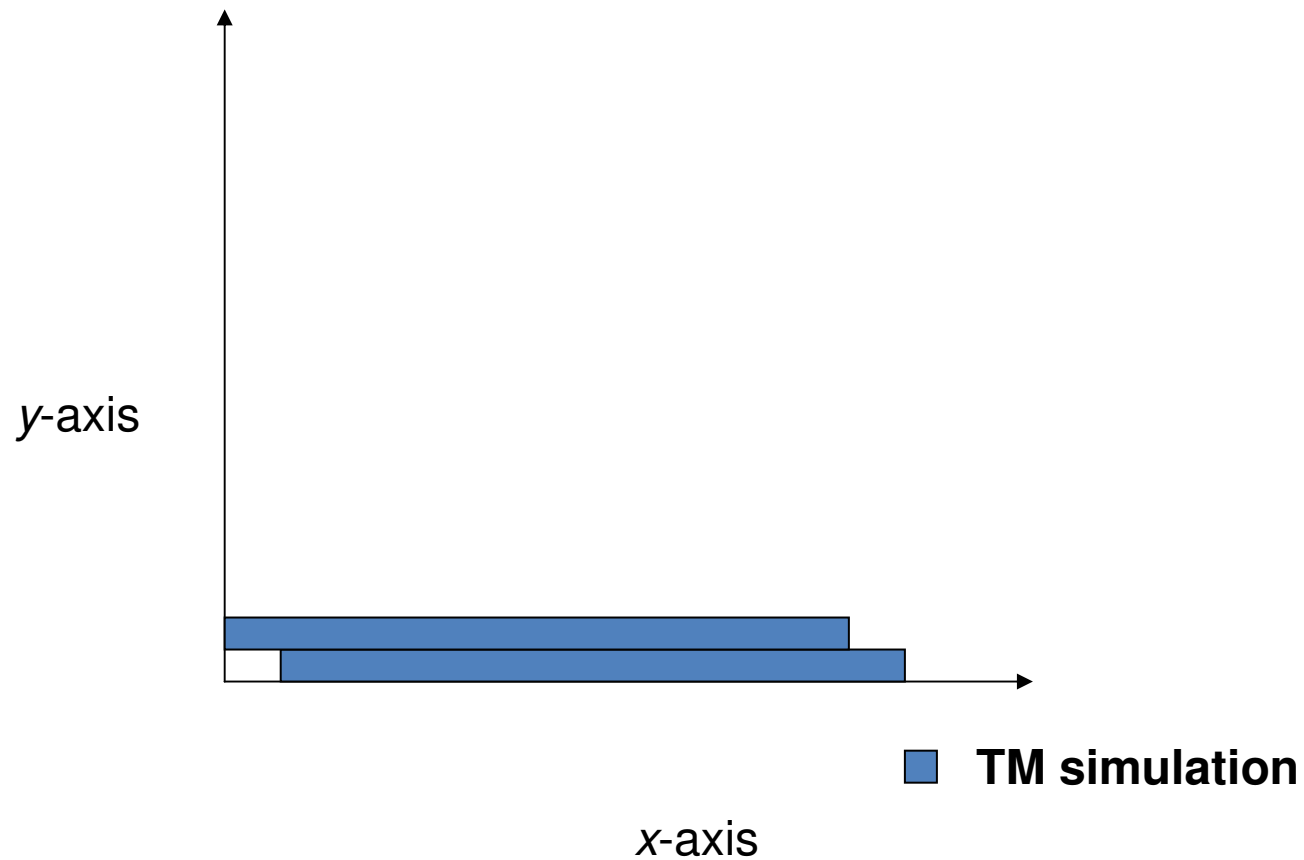


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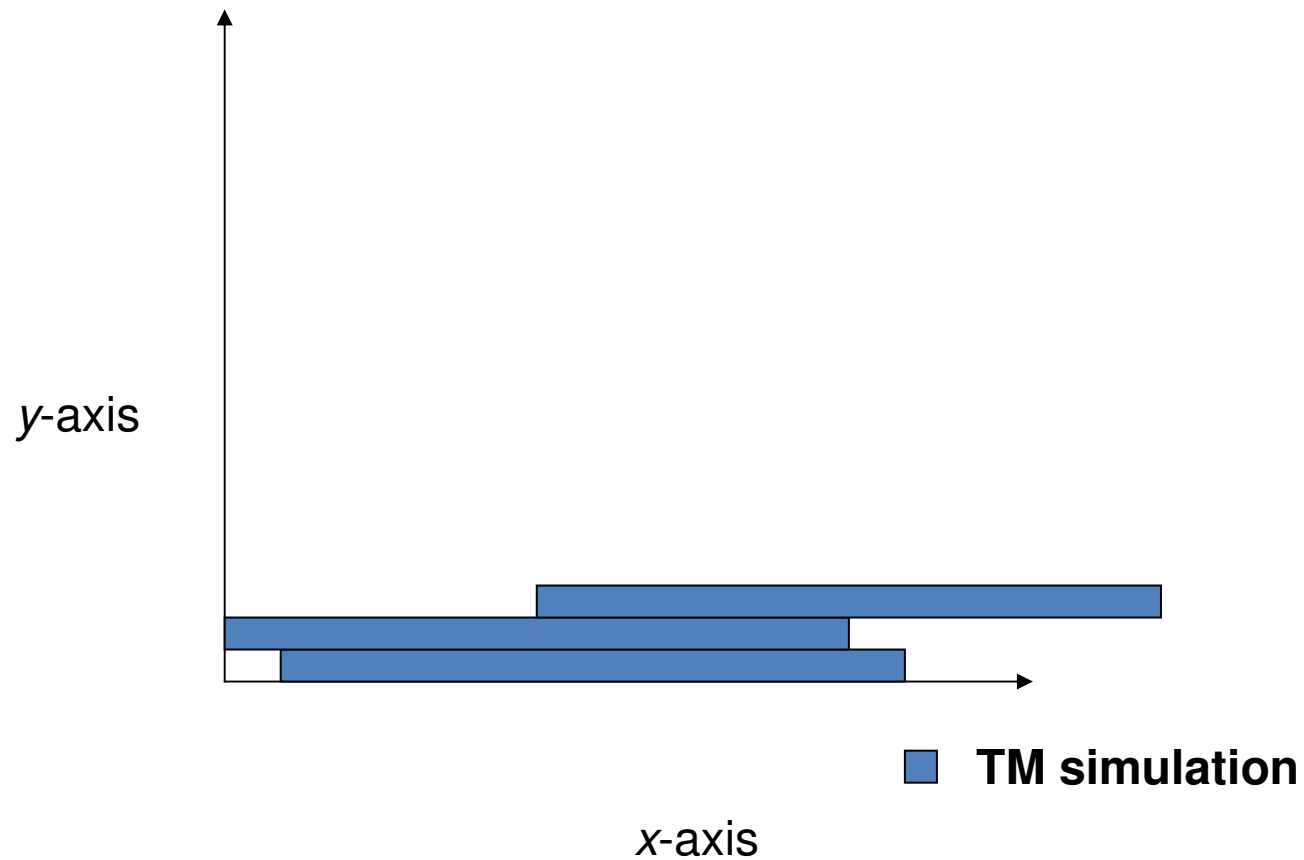




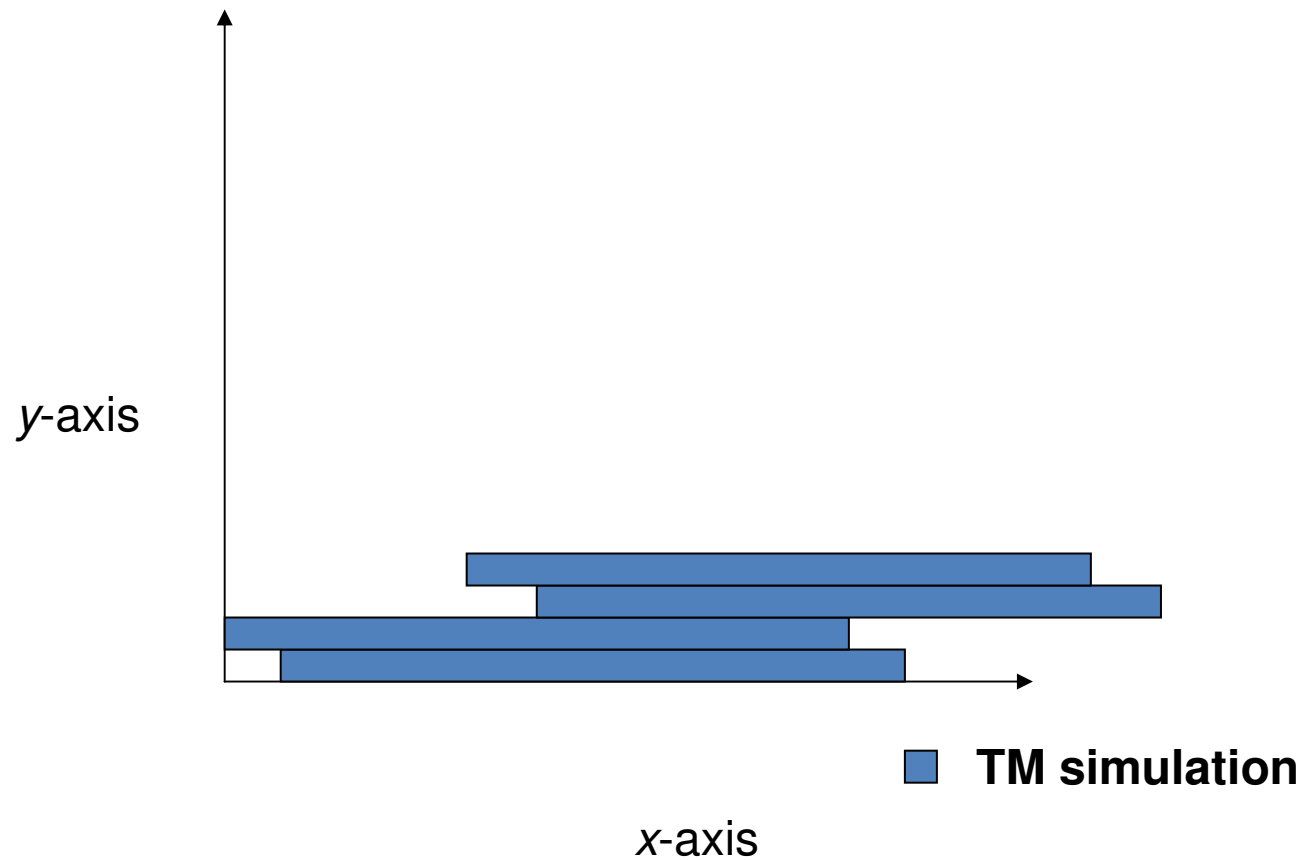
# Second Main Result (proof idea)



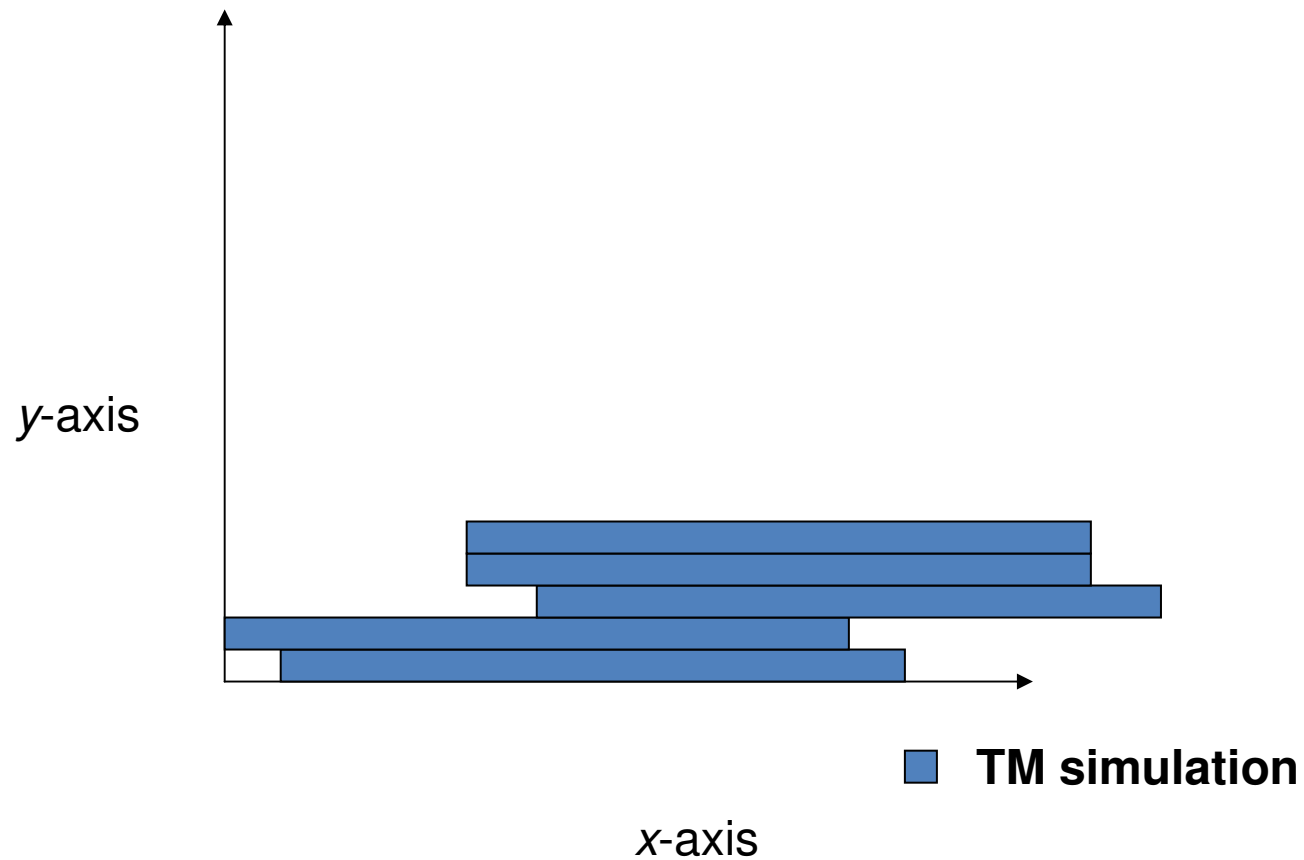
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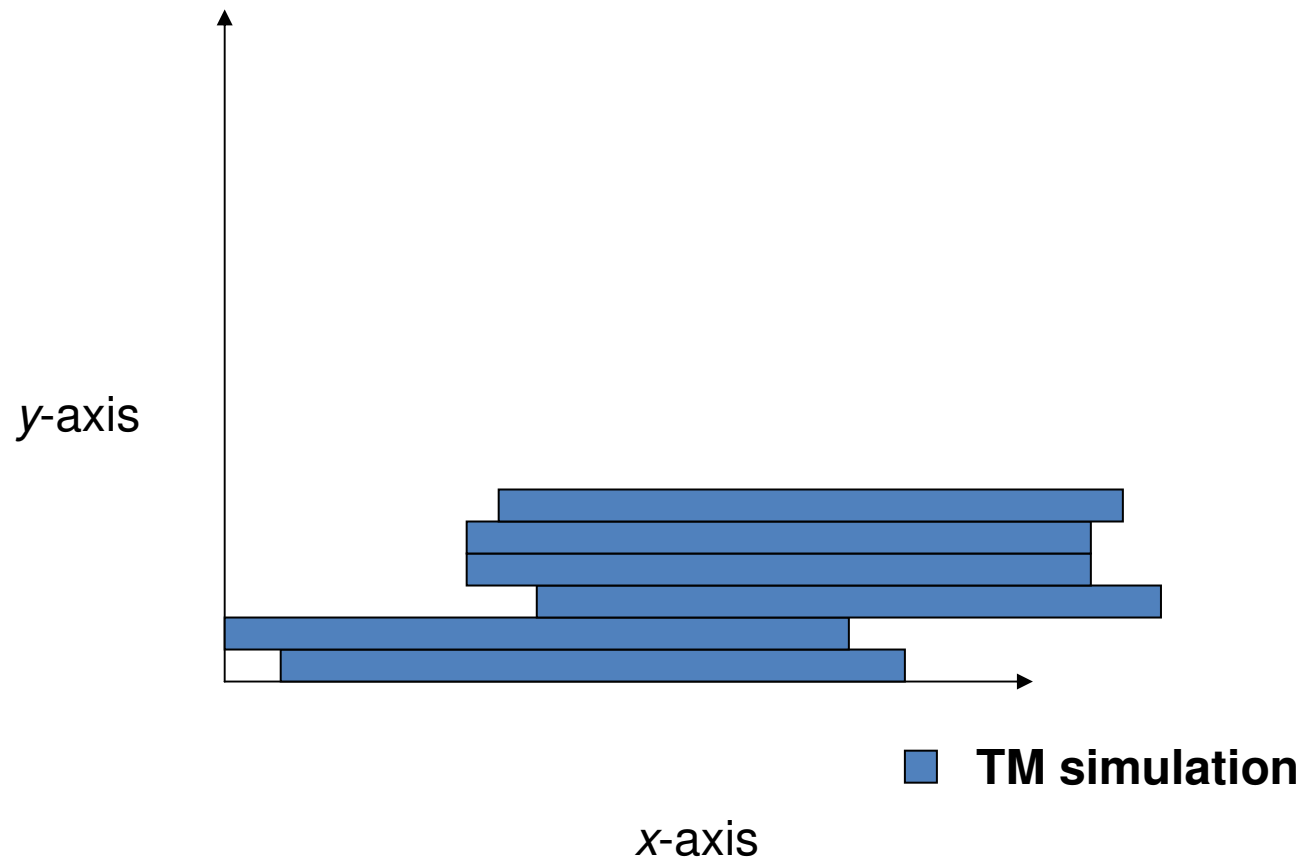
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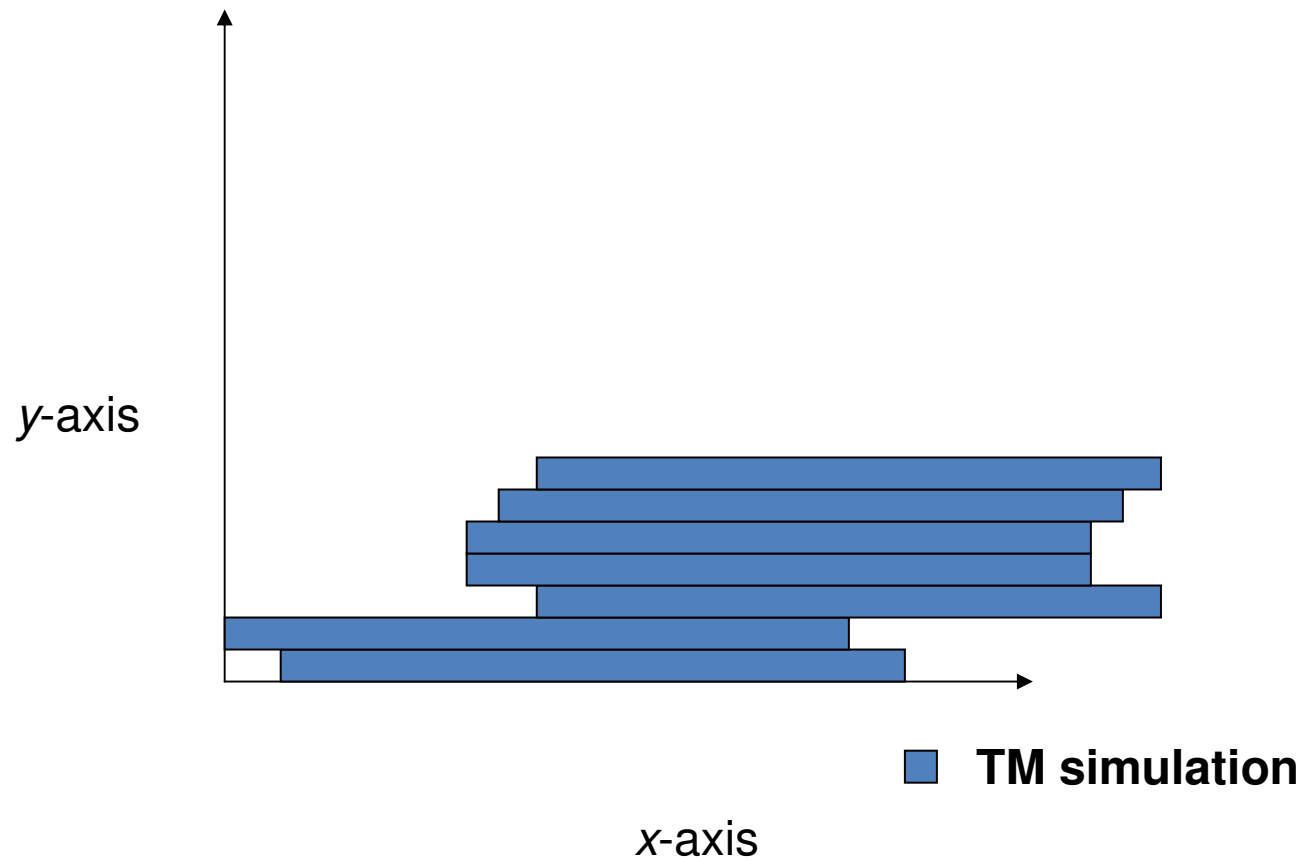
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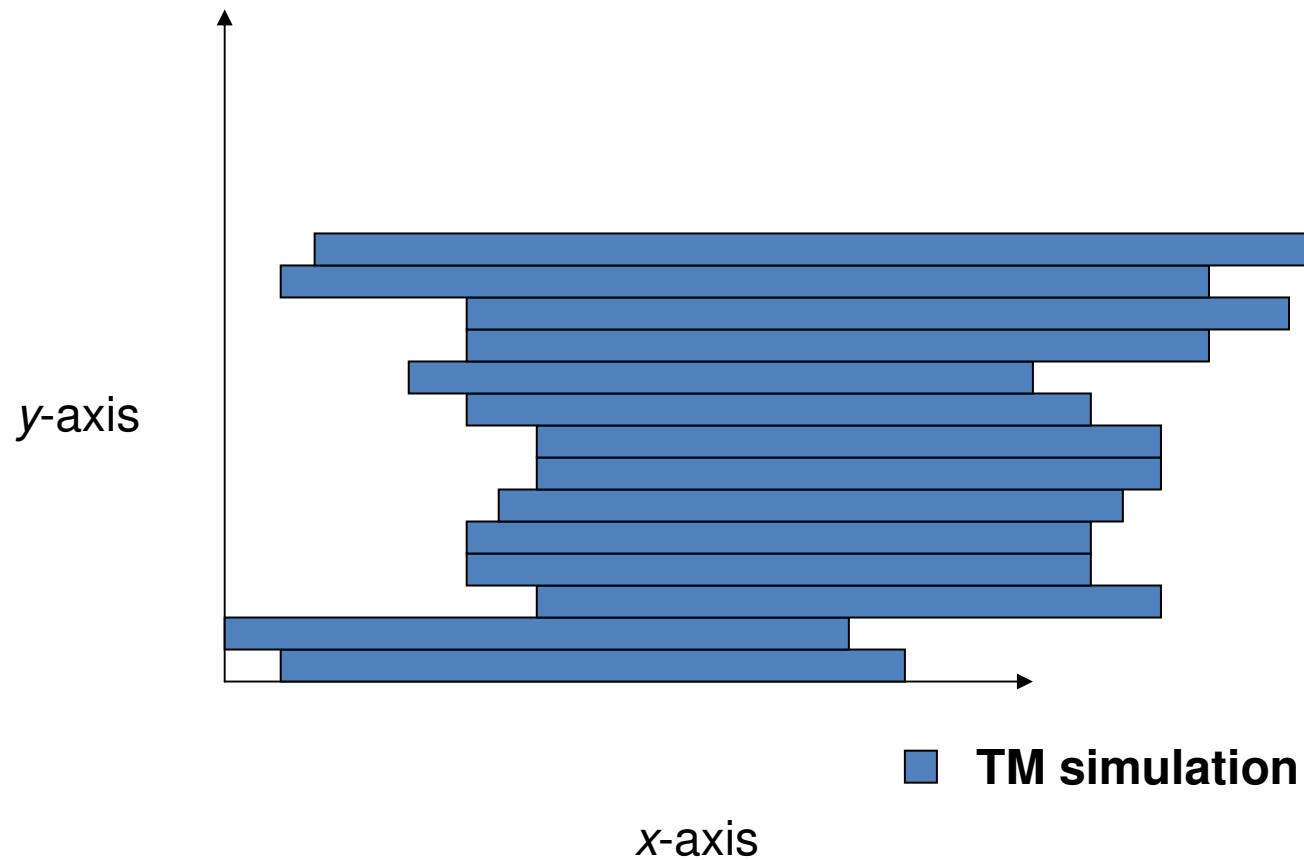
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# Second Main Result (proof idea)

- Proof by contradiction



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- Proof by contradiction
- At least one of the yellow or black paths must turn “left” at some point (otherwise the language has small space complexity)

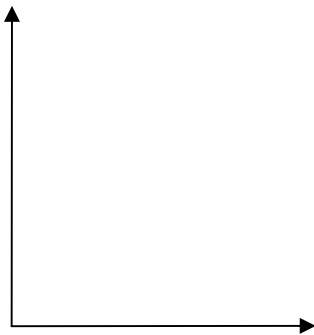
# Second Main Result (proof idea)

- Proof by contradiction
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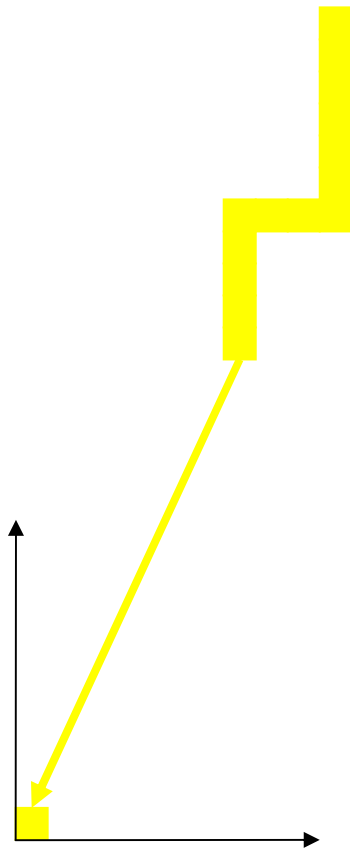
# Second Main Result (proof idea)

- Proof by contradiction
- At least one of the yellow or black paths must turn “left” at some point (otherwise the language has small space complexity)
- Once a single path turns left, all must do so.
- Infinitely many paths turning left must eventually creep into another (adjacent) quadrant.

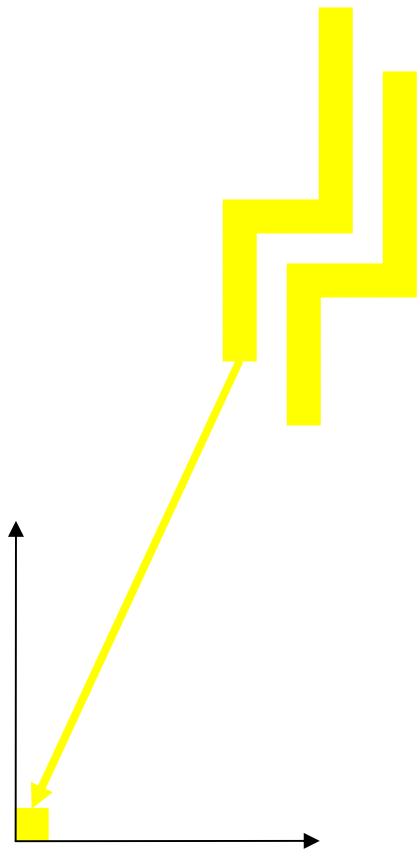
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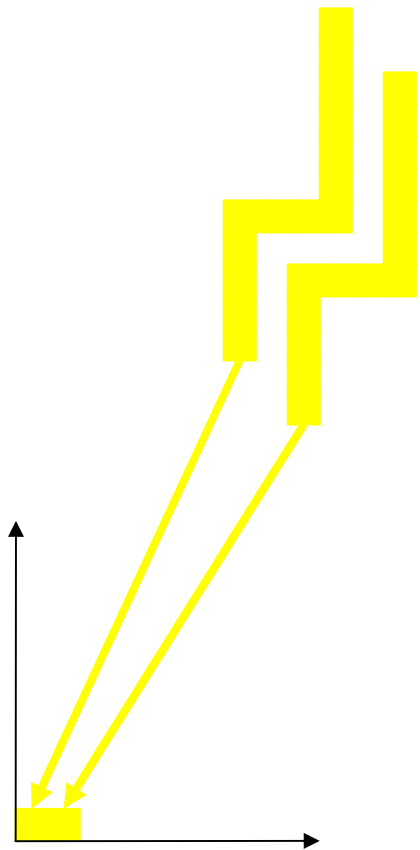
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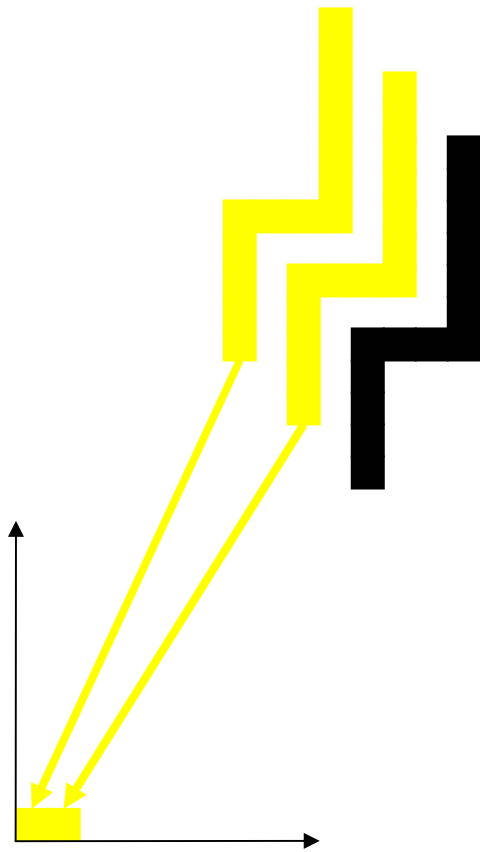
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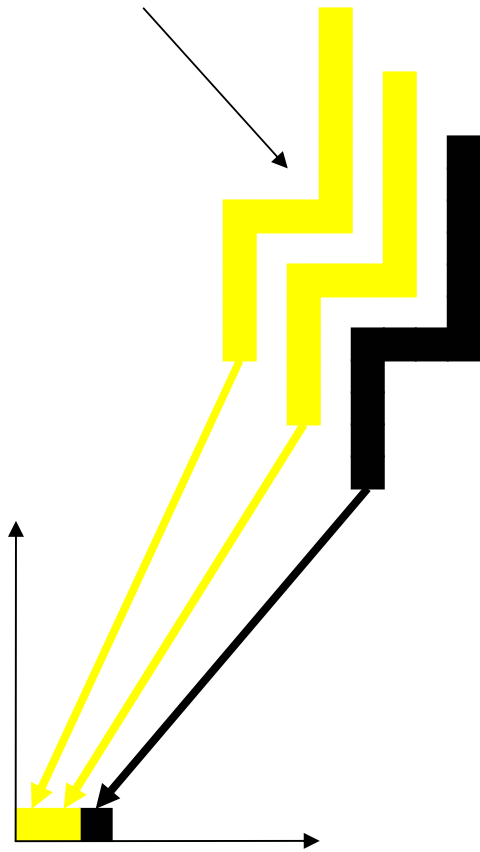


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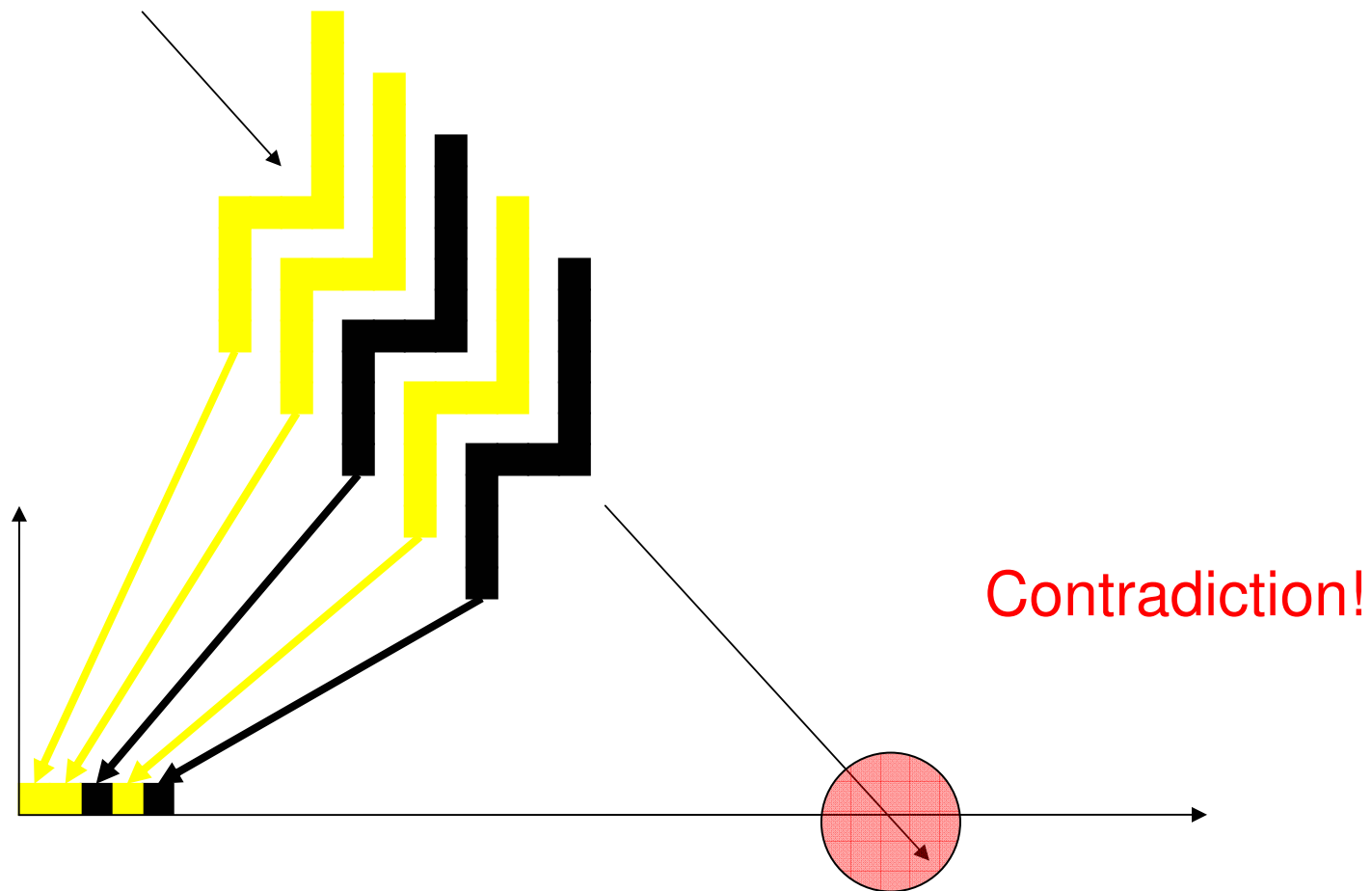


# Second Main Result (proof idea)





# Second Main Result (proof idea)



# Web Site

Software which generates tile sets for the main construction from Turing machine definitions and tile assembly simulation software are freely available for download from the homepage for the ISU Laboratory for Nanoscale Self-Assembly:

<http://www.cs.iastate.edu/~Insa>

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Thank you!